

A TECHNIQUE FOR MONITORING A REAL-TIME MULTI-  
COMPUTER SYSTEM UNDER HEAVY LOADING

by

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# United States Naval Postgraduate School



## THESIS

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A REAL-TIME MULTI-COMPUTER SYSTEM  
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December 1970

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A Technique For Monitoring  
A Real-Time Multi-Computer System  
Under Heavy Loading

by

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Lieutenant, United States Navy  
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# ABSTRACT

Techniques are developed for the design of a monitor of a real-time multi-computer system that is under heavy loading. The first portion relates to the requirements of partitioning to aid in fault recognition and diagnostic routines. The dynamic allocation of system time to the system tasks and fault monitoring is developed secondly. System reconfiguration of the partitioned subsystems restores the system to operation at a degraded level until faults are corrected. The paper discusses a Ship Combat Weapon System as an example of a large scale multi-computer system monitor.



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## I. INTRODUCTION

The monitoring of a small computer system is a relatively simple task. But as computer systems grow larger and larger, the problem becomes much greater. When the monitoring of a large scale Real-Time Multi-Computer system is attacked, many problems arise. One must know which program is running in which computer and how each program affects the mass of data that flows between the many input/output ports of the system. When the system is running near capacity in all processors (heavily loaded), the problem becomes even greater. There is little time left to process data for monitoring. Therefore, it can be seen that monitoring must be done under very severe timing and space utilization constraints. This thesis provides the necessary tools for calculating timing and core space requirements.

The five major portions of a fault monitoring system are (1) Partitioning of the total system into small subsystems, (2) Dynamic time allocation to better utilize remaining time for fault monitoring procedures, (3) Fault recognition techniques, (4) Diagnostic routines, (5) System reconfiguration to automatically restore system operation. All five of these operations are fully discussed and analyzed so that the designer may apply the correct techniques to his system to obtain an effective monitor.

One example of a Real-Time Multi-Computer system is a Ship Combat Weapon System. The general aspects of a Ship Combat Weapon System are discussed to show the comparisons of a general system to a specific one. A simulated Ship Combat Weapon System is shown to contain all



the necessary parts of the general system and is analyzed in great detail. The methods of monitoring this simulated Ship Combat Weapon System are analyzed to develop the necessary monitoring techniques.

A detailed bibliography is presented that covers the area of Monitoring of a Real-Time Multi-Computer System. A cross reference of the methods of monitoring is also given.

Proper system time utilization shows that much of the system upkeep and maintenance that normally is accomplished during designated maintenance periods may now be performed on-line at little or no system degradation.



## II. BASIC ELEMENTS OF A FAULT MONITORING SYSTEM

Fault detection and recognition is the most important maintenance function in a Real-Time System. The Fault Recognition program basically tests the processing integrity of the system. This program requests that the subsystem found faulty or suspected of being faulty be diagnosed. The purpose of the diagnostic program is to generate test data to isolate the fault to a reasonably small section within the subsystem.

Real-time fault detection historically has been done at the circuit hardware level. In the early stages of development of fault-tolerant computers, attention was directed towards massive redundancy at the lowest level - the replication of individual components (resistors, transistors, etc.). The use of component redundancy has been limited by design difficulties and by new developments in component technology. The change from discrete components to integrated circuits has largely invalidated the assumption of independent component failures. Without it, the advantages of component redundancy are lost.

The most developed techniques are fault detection by periodic diagnosis and the application of parity and similar error codes to detect or correct errors in data transmission and storage. The periodic diagnosis techniques have progressed from exclusively software implementations to software combinations with special-purpose hardware. Concurrent diagnosis uses error detecting codes and monitoring circuits.



## A. HARDWARE

Fault detection and diagnosis by hardware have greatly increased the sensitivity and selectivity of finding and correcting errors. In the early days, fault detection systems utilized registers that read data at specific times into an output device.<sup>[1,2]</sup> Later specially built hardware devices set off alarms when an error was detected.<sup>[3]</sup> The circuit that produced the fault was located by utilizing a book which contained manual diagnosis. As designers progressed, circuits were designed to not only send interrupts to the computer notifying it of an error, but also allowing the diagnostic routine programmatic access of the error register.<sup>[4,5,6]</sup>

To assist in locating faults, the hardware system may be partitioned into logical subelements that allow reconfiguration. Accurate timing of events requires a real time clock in the system. Transient or permanent faults may be initially detected by hardware devices but efficient identification and location of the faults requires software diagnostic routines. Diagnostic routines are built upon the concept of detecting faults by executing one instruction at a time. As the instructions become more complicated, more circuitry (Microsteps) is analyzed for faults. This is repeated until all elements are insured to be fault free or a fault is located.

It can be seen that to effectively detect an error in a timely manner requires hardware circuits. Programmatic access to error registers allows greater flexibility and speed in diagnosing the actual fault.





## B. SOFTWARE

Many studies and investigations have been made in the area of fault-detection and diagnosis by software. Even the earliest computer systems had diagnostic programs to check the computers for errors.<sup>[7]</sup> As computers became more complex, the size of diagnostic routines increased as did the time required to write them (in man years).<sup>[3,5,8,9]</sup> By combining fault-detection with diagnostic routines, the total time to locate an error was reduced. By allowing periodic maintenance checks to be performed using this combined method, large computer systems reduced their amount of down time.<sup>[10,11,12,13]</sup> By combining fault-detection and diagnostic routines with automatic system reorganization, the down time may be reduced to a minimum with imposed system degradation.<sup>[4,14,15]</sup>

Software must also be partitioned into logical subelements to allow for program relocation or reconfiguration. Knowing when to reconfigure requires monitoring the most critical data of each logical subelement program. When critical data of a program are detected as being faulty, then the program itself is faulty.

When the combined technique of fault-detection, diagnostic routines and automatic system reorganization is used in a large system, additional problems occur. Finding time to run the required tests is a problem in a heavily loaded system. If there is barely enough time to complete the required tasks, how can we allow extra time for maintenance tests? The answer implies some type of dynamic time allocation. Another problem is the manner of presenting this detailed data to the system monitor operator in a timely manner. The operator must have enough data, but in a short time, to allow him to complete the action required of him before the total system fails.



### III. DEFINITIONS AND CONCEPTS OF FAULT MONITORING IN A MULTI-PROCESSING SYSTEM

Large and complex computer systems increase the demand for fault monitoring systems. Because of this complexity, man by himself requires too much time to solve the same problem. The cost of uncorrected errors is especially severe in large multi-computer systems and in situations which a computer controls a very valuable system, and is not readily accessible for human repair. Examples are a real-time control computer and a spacecraft computer controlling an inter-planetary mission. A second critical requirement for fault monitoring exists when human lives may be affected by computer errors, (e.g. military defense systems, high-speed transportation control systems, or medical systems).<sup>[14]</sup> The time to repair such complex system must be reduced, and fault monitoring is one approach.

The five necessary parts of fault monitoring are: (1) Partitioning, (2) Dynamic time allocation, (3) Fault recognition, (4) Diagnostic routines, (5) System reconfiguration. These must be considered in detail so that the total effect may result in an efficient optimal system monitor. The following description describes the necessary elements of a fault monitoring multi-computer system. Each part of fault monitoring will first be defined in detail. Secondly, some explicit uses of these parts will be given for fault monitoring.

#### A. PARTITIONING

Partitioning is the process by which a large complicated system is divided into logical subelements. Each subelement has a specific



function in either hardware or software. By requiring each subelement to be a logical subdivision, it may then be replaced in case of a detected failure.<sup>[4,10,16]</sup> An example is a program subroutine that is located in faulty computer memory core; it may be relocated to a fault free core location. Another example is a detected fault in an input/output (I/O) channel; the monitoring system would reconfigure the system input/outputs so as to utilize another channel. If a Central Processing Unit (CPU) failed, its tasks (processing of logical program subelements) could be allocated to other CPU's in a reduced operating mode.

The most important requirement for successful partitioning is to segment the system into logical elements, each having the capability of being relocated by the system reconfiguration module.<sup>[4,14,15]</sup> Thus it is required that core memories, for example, be divided into modules, regardless of the actual computer memory organization. The hardware items could be partitioned into CPU's, core memory modules and input/output channels. Thus when one hardware item fails and another similar hardware unit is free, (or only partly utilized) it may be used immediately by the system monitoring reconfiguration program. In a similar manner, all computer software programs and subprograms should be partitioned into logical units of approximately equal core size so that immediate reconfiguration may be performed. Programs of unequal size would create the problem of moving all programs in the computer. If a display device should fail and the display processor program becomes idle, the system could be reconfigured to use the teletype (or some other output device) along with the teletype processing module.



## B. DYNAMIC TIME ALLOCATION

Time slicing is the division of the total computer system time available. This system time is allocated to all of its component parts and programs.<sup>[12,18]</sup> Time slicing is used to minimize the time required for fault detection and diagnosis in any one time frame. During routine system operations when the system is lightly loaded, there are large blocks of time available in each executive cycle for fault recognition and diagnostic analysis. As the system becomes more in demand, the time available for analysis is shortened. In order to utilize this time more effectively, the fault monitoring program must dynamically allocate the available time. Thus the program must know how much time is available for use and thus how much diagnosis may be performed in this specific cycle. Flags (or some other method) must be set and the proper bookkeeping performed to insure that the most critical data is still monitored and analyzed during the most heavily loaded period of system utilization. During lull periods, the monitoring program must also insure that all components of the computer system are analyzed so as to insure complete system integrity.<sup>[12,13]</sup>

It follows directly that fault detection routines must be timed and must be able to operate under flag (executive) control. By the proper allocation of these routines to the pertinent tasks at hand, all criteria may be satisfied. Strict timing control of the main and monitoring program must be performed and must be programmatically available. 13

By dynamically controlling the time used for fault monitoring, a wide range of operational modes may be effectively monitored. These





modes may vary from lightly loaded systems to very heavily loaded systems. In a lightly loaded system most all operations are concentrated towards fault monitoring, diagnostic analysis and maintenance. Under heavy loading, the time for monitoring is very small. In most systems, it is zero except for hardware fault monitors. By dynamically allocating a small segment of time to special purpose fault monitoring routines, increases reliability may be gained with little system interference.

### C. FAULT RECOGNITION

Fault detection in digital computers is implemented either by periodic or by concurrent diagnosis. The most common current approach is periodic diagnosis which utilizes a diagnostic program stored in the computer memory.<sup>[6,17]</sup> Computation is periodically interrupted and the diagnostic program is executed. The diagnostic program itself is vulnerable to faults in the memory system. The cost of diagnosis consists of: (1) the storage used for the diagnostic program, (2) the time consumed by its execution, (3) the time needed for repair, (4) the repeated execution (rollback) of the program segment which was run after the last diagnosis.<sup>[14]</sup> Such time and storage costs are very severe in real-time computing. The alternate diagnosis method is concurrent diagnosis in which error-detecting codes and monitoring circuits are employed to indicate the presence of faults.

A distinction must be made in fault detection between transient and permanent errors. By maintaining a history of detected errors with no diagnosable faults, a trend of transient errors may be stored. This trend may be utilized to determine an impending major fault. In



critical locations, special hardware devices must be installed to detect errors that would remain undetected or unrecoverable by software diagnosis. For example, the current instruction address, in the location counter, may be required to locate a fault. If the location counter is not programmatically available concurrent with the fault detection, then a special fault location register is required. This register would automatically copy the contents of the location counter at the time of any fault.<sup>[4,14]</sup>

Faults may originate from either hardware or software. They may also be detected by either hardware, software or a combination of both. A list of all the pertinent errors to be detected is required. From this list a division must be made between the two types of fault origin, hardware or software. This decision is influenced by the method of fault detection. This list is used in the process of determining partitions. Each fault detection technique used depends upon many system factors that must be taken into account. How the system is partitioned affects the grouping of faults. This grouping of faults is used by the dynamic time allocation routine.

#### D. DIAGNOSTIC ROUTINES

The diagnostic program is designed to isolate and specify errors in main-frame arithmetic and control logic, the various information transfers, the various devices and registers. The program is constructed on the general basis that every command in the machine repertoire uses a unique set of microinstructions or microsteps, leads to a correct result and a second command using the same set plus one leads to an incorrect result, then the failure is assumed to



be in the additional microstep. The detailed diagnosis of errors requires that the possibilities of control signal failure, transmission path failure, and register failure, be investigated. It is often difficult to separate these types.<sup>[4,5,14,15]</sup>

For a large scale real-time multi-computer system, fault diagnosis routines become even more complex and time consuming than the diagnostic routine just described. To eliminate these problems, a modularized systems approach must be utilized. Rather than be concerned with a specific circuit element failing, we concentrate on detecting modular subsystem errors. Critical data are monitored so as to indicate failures in any one of our subsystem modules. Upon confirming a permanent error in a module, the system reconfiguration program is called.<sup>[4,14]</sup>

#### E. SYSTEM RECONFIGURATION

When permanent faults are detected and analyzed in a complex system, the total system may halt or the system may be reconfigured to avoid the faulting component (partitioned submodule) and operated at a reduced level.<sup>[4,14,15]</sup> As discussed before, halting a valuable system is not acceptable. Reconfiguration may be manual or automatic. In the automatic mode, the computer system must maintain a current configuration list of all submodules and their operational status. Upon notification of a submodule failing (that is part of the operational system), reconfiguration is forced. When a submodule is repaired, and proven operational, the system monitor operator may indicate this fact to the program and then request reconfiguration.<sup>[13]</sup> In the manual mode, the system must be examined manually, the new configuration determined and then manually implemented.



The reconfiguration program contains the necessary information to logically interconnect all submodules and to relocate computer programs when necessary. By displaying the proposed reconfigured computer system to the system monitor operator, approval may be given and the new system implemented. Automatic reconfiguration could save as much as 99 percent of the time required for manual reconfiguration.

While automatic reconfiguration gives an indication of overly complicating the problem, it is in reality a simplification. To manually maintain the configuration control of a large multi-computer system is a large team effort. Many charts, manuals, and switches must be coordinated with exacting precision. Automation of this task reduces this problem. The system configuration is maintained up to date in the computer memory. Switching and logical control of the input/output ports are controlled by computer subprograms. When these aids are implemented in a reconfiguration module program, the time required to change a configuration is reduced to seconds.

Because of the hypercritical nature of the fault monitoring process, special precautions must be observed. The fault monitoring program can be duplicated in another computer or reside in a special fault tolerant computer. These precautions reduce the danger of a fault occurring during execution of the reconfiguration program.





#### IV. A SPECIFIC EXAMPLE: A SHIP COMBAT WEAPON SYSTEM

One example of a complicated real-time multi-computer system is a ship combat weapon system. Many computers are utilized to solve special subsystem tasks, such as target detection and missile firing. The total computer complex is integrated together by the Combat Information Center System. In this real-time system, many control and data processing functions must be performed at extremely high speeds. Most of the sensory and control devices must be electrically connected on-line to the system to permit automatic transfer of data. Delays in transferring data by means of manual off-line handling of tapes, cards, etc., are not acceptable.

An executive control philosophy was developed for distributing the various tasks among the computers. Control of these tasks in each computer is maintained by an Executive Routine. Over-all control of the multi-computer system is not employed since each computer is controlled by its own Executive Routine. Subroutines or tasks are controlled by the Executive Routine by the use of flags or alerts. Decisions on whether to respond to the flag or alert at any specific time are determined by the priority of the input and the time available to do the task. This accurate timing is made possible by use of an internal real-time clock. When each task is completed, the flags and alerts are sensed again and the highest priority task remaining is performed. Both periodic and demand type tasks are utilized by executive routines.



A Ship Combat Weapon System is an example of a large real-time multi-computer system in actual use. A detailed study of this system will be explored for the methodology of Fault Monitoring techniques. The methods evolved for Fault Monitoring in a Ship Combat Weapon System will apply towards most large real-time multi-computer systems.

#### A. PROPERTIES OF A COMBAT WEAPON SYSTEM AND THE DATA INVOLVED

A typical Ship Combat System consists of many interconnected systems (see fig. 1). They are divided into three major areas: (1) Input, (2) Processing and (3) Output. All different types of input devices are analyzed by the input processors and the relevant data transmitted to the processing section. The processing section correlates the data from the different sensors. The correlated data is transferred to the appropriate output device (Guns, Missiles).

Each of these three areas have in turn many components, some of which are:

##### 1. Inputs

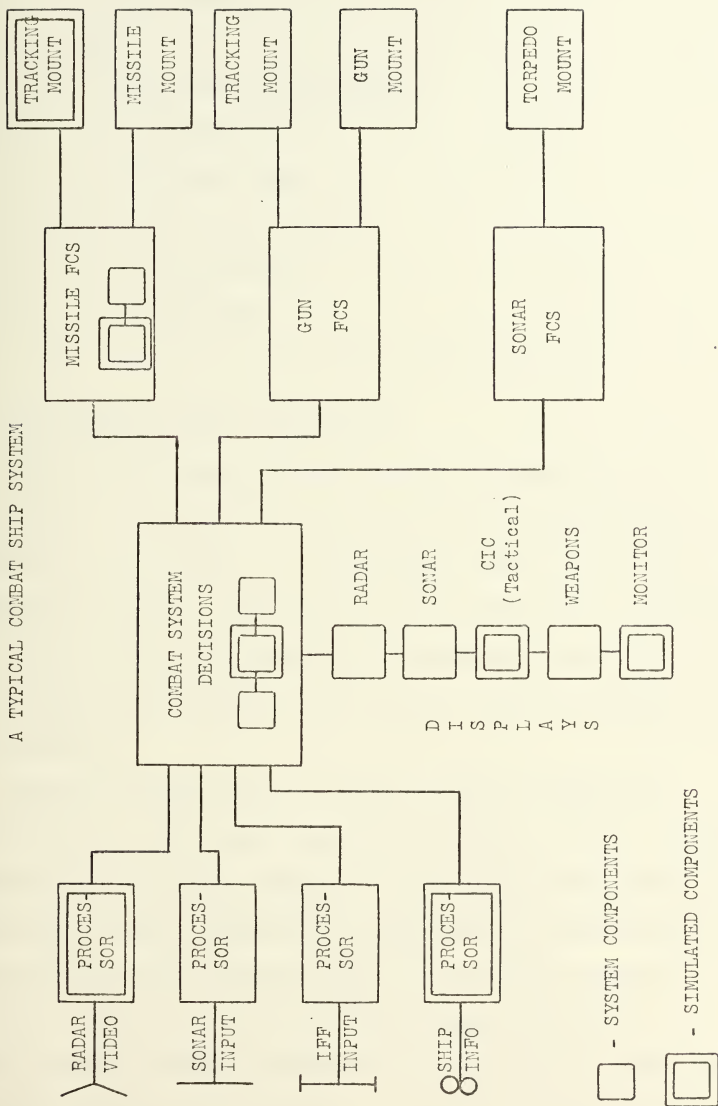
- A. Radar video
- B. Sonar input
- C. IFF input
- D. Ship information

##### 2. Processing (Combat System Decisions)

- A. Three processing computers
- B. Five types of display consoles
  - (1) Radar
  - (2) Sonar
  - (3) CIC (Tactical)



FIGURE 1  
A TYPICAL COMBAT SHIP SYSTEM





(4) Weapons

(5) Monitor

### 3. Outputs

#### A. Missile Fire Control System

(1) Tracking mount

(2) Missile mount

#### B. Gun Fire Control System

(1) Tracking mount

(2) Gun mount

#### C. Sonar Fire Control System and Torpedo Mount

### B. THE SIMULATED COMBAT WEAPON SYSTEM

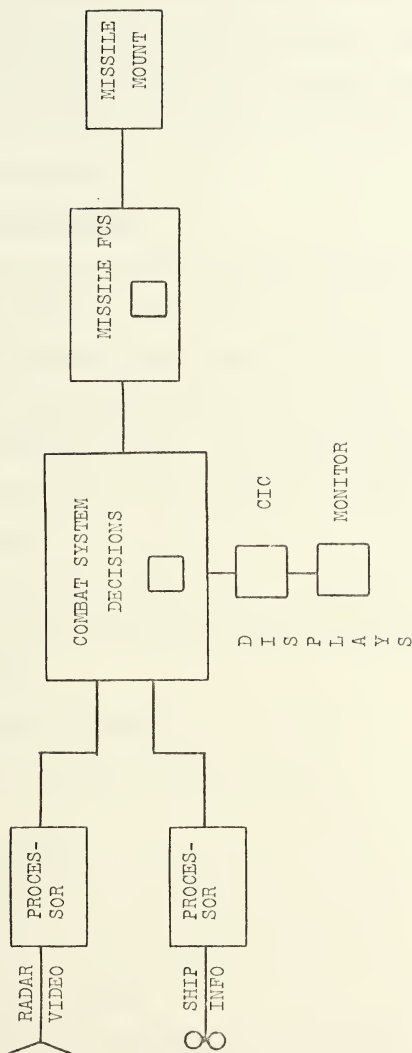
The Ship Combat Weapon System described in figure 1 is very complicated. To analyze this system in detail for Fault Monitoring purposes is a large task. A representative system will be simulated instead (see fig. 2). The simulation will contain Input, Processing and Output sections. By including one system with each function, a representative but reasonably sized, heavily loaded system may still be simulated.

The Naval Postgraduate School has the unique computational facilities of a large system simulation laboratory with three digital computers and one analog computer: the Xerox Data Systems (XDS) 9300 medium scale digital computer, two Adage Graphic Terminals AGT-10, and the Comcor CI-5000 analog computer. Each digital computer is assigned a major task of the Ship Combat Weapon System while the analog computer (CI-5000) simulates the physical missile mount. The physical identity of the computers, the system functions and the computer programs may be identified in figure 3.





FIGURE 2  
SIMULATION OF THE SHIP COMBAT SYSTEM





The Adage Graphic Terminal computer number one (Adage 1 computer) contains the Radar, the Ship processor and the Monitor programs. Adage Graphic Terminal computer number two (Adage 2 computer) contains the Combat System Decisions program. The XDS-9300 computer contains the Missile Fire Control System Program and the CI-5000 operates as the Missile Mount Simulation.

#### 1. Types of Data Involved

Some typical types of data involved in a Ship Combat Weapon System are shown in table 1. Each of the five sections of table 1 are typical of the data that each system would contain. This data is utilized by the Fault Monitoring program to analyze the Combat System for the detection of errors. The fault monitoring program, will in turn pass this data to the Diagnostic program for further analysis and evaluation.

The following types of data may be sampled for error detection and fault location by an indication of large jumps in the data.

1. Radar azimuth
2. Target range and bearing
3. Gyro position, azimuth, pitch, and roll
4. Speed
5. Intercept point
6. Time to fire
7. Time to go
8. Launcher angle ordered
9. Launcher limits, data sample rate and bearing
10. Missile mount bearing and elevation

Normally, this data would be a continuous stream.



FIGURE 3 PARTITIONED HARDWARE \* - SIMULATED

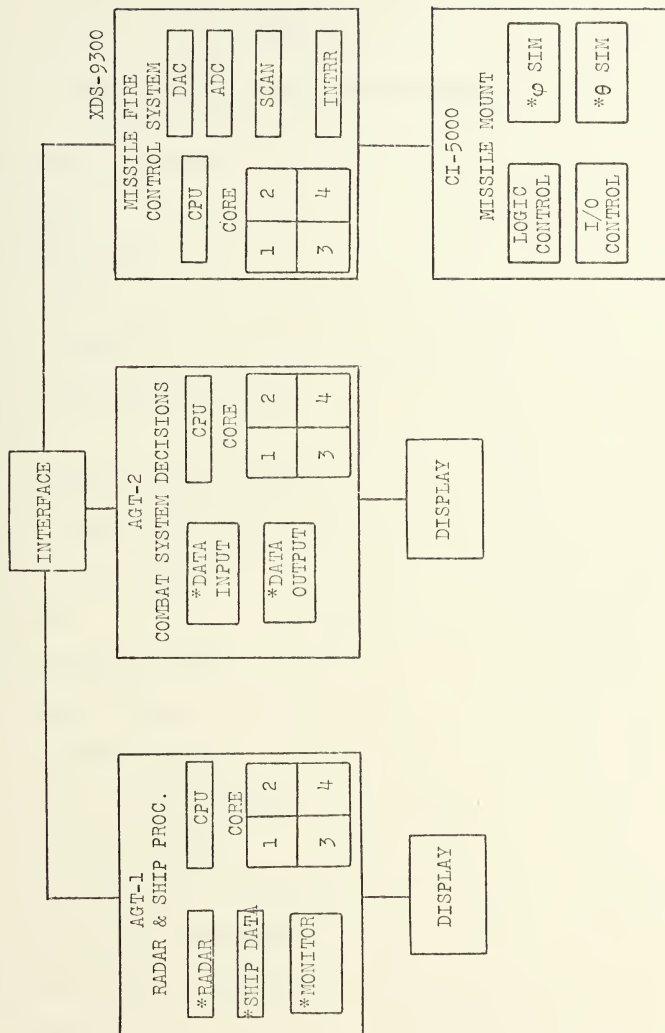




Table 1

Types of Data in the Simulated System

1. Radar Video & Processor
  - A. Azimuth
  - B. Target Data
    1. Range
    2. Bearing
2. Ship Information & Processor
  - A. Gyro
    1. Position (Lat., Long.)
    2. Azimuth (Heading)
    3. Pitch, Roll
  - B. Speed
3. Combat System Decisions
  - A. Computer Status
    1. Memory Available
    2. I/O Channels available
    3. CPU's available
  - B. Target Data (Speed, Heading)
  - C. Total System Configuration
4. Missile Fire Control System
  - A. Intercept Point
  - B. Time to Fire
  - C. Time to Go
  - D. Target Destruction Evaluation





(Table 1 Cont.)

- E. Launcher Angle Ordered
  - F. Launcher Data Sample Rate
  - G. Launcher Bearing Limits
5. Missile Mount
- A. Bearing ( $\theta, \dot{\theta}, \ddot{\theta}$ )
  - B. Elevation ( $\phi, \dot{\phi}, \ddot{\phi}$ )
  - C. Operational Status
    - 1. Errors in Bearing & Elevation
    - 2. Drift Rates



Another method of error detection is by the analysis of data outside of some physical limits.

1. Angles greater than  $360^{\circ}$  or negative angles.
2. Target data outside of radar range or negative range
3. Pitch greater than  $\pm 20^{\circ}$
4. Roll greater than  $\pm 90^{\circ}$
5. Ship speed greater than  $\pm 100$  knots
6. Acceleration greater than reasonable limits

A few general rules may be given to assist in the detection of faults.

1. Monitor data normally assumed to be continuous and smooth.

When large deviations are detected, an error has occurred.

2. Monitor physical data and check for data outside physical limitations, i.e., ships moving faster than 100 knots.

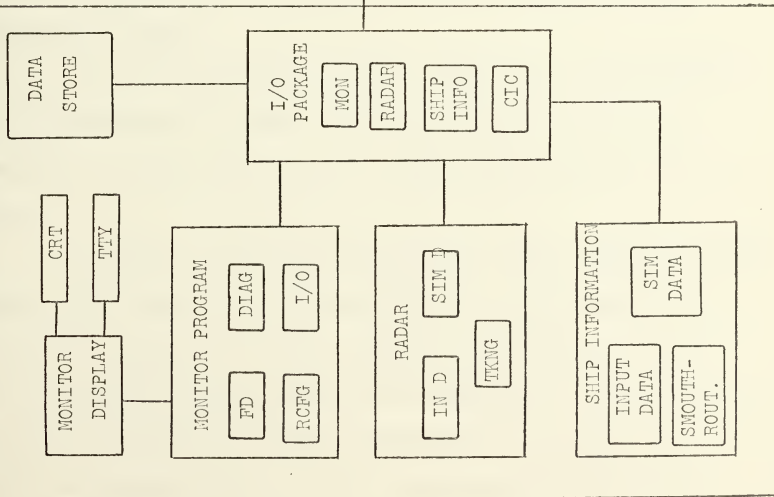
3. Send test data to software subroutines and analyze the results.

4. Send test instructions to the computers to check for central processing errors.

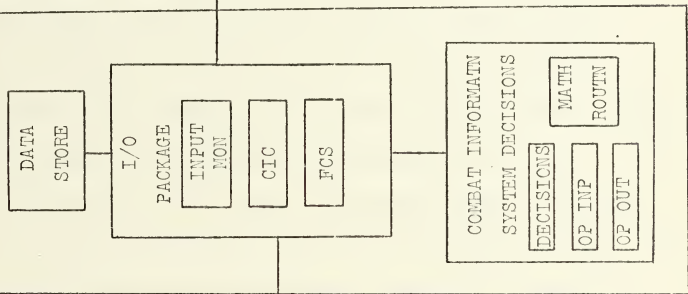
The program interaction of the Ship Combat Weapon System is described in figure 4. Note the generalized use of the I/O package for data exchange between programs. This general use of a common program simplifies the automatic reconfiguration program. Since all programs have the same requirements of input and output, then data handling is the same. When data from one program looks the same to another program, then program relocation is simplified. A program may be moved from one computer to another without a change to the I/O package program. Data sampling and fault monitoring also become simpler for the same reason.



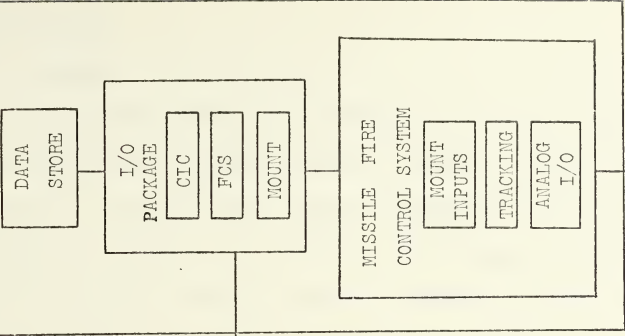
ADAGE - 1



ADAGE - 2



XDS - 9300



TO CI - 5000

FIGURE 4 SOFTWARE PARTITIONING



## 2. Partitioning

### a. Hardware

The hardware items of the simulated Ship Compat Weapon System were partitioned in section III into four major sections and many subsections. The four major computer subsystems have become the main hardware partitions. (see fig. 3) Each digital computer is composed of subsections normally ascribed to digital computers; Central Processor Units (CPU), magnetic core, display units and input/output channels. Some items like Radar, Monitor and input data have been simulated with software routines for lack of the actual hardware devices. Because of the similarity of program size and overall program action, partitioning by subsystems is a reasonable choice. By monitoring critical data within these subprograms, fault monitoring of a subsystem becomes simpler than monitoring the system in total. If any critical data from the radar subsystem is detected as erroneous, the direct assumption by the diagnostic routine is that the radar subsystem is at fault.

The analog computer contains a simulation of the Missile Mount and therefore, the actual gear train and motor systems are simulated. The logic control and I/O control are hardware accessible devices.

Note the similarity of the hardware items among the computer systems. This allows a more direct method for fault detection and analysis. Since all three digital computers have central processor units and all have modular computer core memories (or simulated ones), then they are similar for hardware partitioning. One module of a computer core memory could be used to replace another that contains





faults. The replacement could be in the same computer or in alternate computers. Automatic reconfiguration is possible with similar interchangeable subsystems.

Table 2 describes the critical data points in the Combat System used to detect the various hardware errors. With data monitoring of these hardware devices, any error may be analyzed to determine the actual device at fault.

#### b. Software

Programs have been partitioned into the same four major partitions as the hardware partitioning. Each subsystem function, such as Radar or Ship Information, is used to separate major partitions. (see fig. 4) The detailed partitions are different tasks within each subsystem function, such as Input data, Simulation data or Tracking. Because each partitioned programming task is of approximately the same magnitude, relocation is greatly simplified. Upon software program reconfiguration, the first step will be to reload the subprogram at fault into the same computer as the one it first faulted in. If this fails, the program will be reloaded into another computer with available space. Software reconfiguration will be completed faster this way than by reloading the total system.

The pertinent software errors for fault detection are shown in table 3. By diagnosing any of these errors, the faulting subprogram may be easily determined in a short time. The subprograms may then be relocated by the system reconfiguration program.

### 3. Interface Requirements

#### a. Data Paths

In order to examine the exchange of data in a detailed



Table 2

Hardware Errors to Detect

1. Radar
  - A. Azimuth
  - B. Target Data (range, bearing)
2. Gyro
  - A. Heading
  - B. Pitch, Roll
3. Pit Log
  - A. Speed
4. Memory
  - A. Read, Write
5. I/O Channels
  - A. Parity
6. CPU
  - A. Incorrect subroutine answers
7. Missile Mount
  - A. Launcher Angle
  - B. Bearing
  - C. Elevation
  - D. Drift rate



Table 3

Software Error Detection

1. Ship Information
  - A. Intercept Point
  - B. Time to Fire
  - C. Time to Go
2. Radar
  - A. Target Data - Speed, Heading
3. Gyro
  - A. Latitude
  - B. Longitude
  - C. Speed



manner, the actual electronic interface must be minutely analyzed. Table 4 describes the interface equipment that is involved in the system simulation program. Note the two different levels of core memory accessibility that are detailed for the XDS-9300 computer, (accessible and inaccessible). This is very appropriate since the modern modular computer system utilizes this technique of memory protection. Any data in this protected area of memory must first be accessed from inaccessible core and placed in accessible core.

Six levels of data accessibility are described in table 4 to account for all possible types of interfaces. Some computer systems may only have one or two levels; the more complex systems may include all six types. Systems of all complexities will be represented by these levels of interfacing.

Table 5 lists the data paths and gives for each the access time, hardware path, interface interference and typical types of data that would be retrieved. The data paths are the same as shown in table 4. The access times are the actual times required for both the software and the hardware. The first time specified is a fixed time, the second is the time for each additional access. The column described as "hardware path" describes the actual path the data takes through the system. The right arrow ( $\rightarrow$ ) shows the path of the data from one hardware item to another as abbreviated in table 4. The interface interference describes the interruption that the data access causes to the other hardware and software systems. The typical types of data retrieved are as described in table 1.





Table 4

## INTERFACE EQUIPMENT COMPONENTS

	Abbreviation
1. Adage memory (core)	AM
2. Adage I/O program	AP
3. Interface box (Adage to 9300)	IB
4. 9300 memory, accessible (8k-32k)	XMA
5. 9300 memory, inaccessible (0-8k)	XMI
6. 9300 I/O program	XP
7. Hybrid interface box	HI
8. Analog Computer (CI-5000)	AC

## DEPTH OF DATA ACCESS (see table 5)

(Based on Monitor in Adage 1)

1. Directly addressable	Adage memory (core)
2. Indirectly addressable	Accessible core in 9300
3. Programatically accessible (level 1)	Inaccessible core in 9300
4. Programatically accessible (level 2)	Alternate Adage core
5. Programatically accessible (level 3)	Analog data (DAC-ADC)
6. Programatically accessible (level 4)	Analog data indirect (by SCAN system)



TABLE 5

DATA PATH	SOURCE	ACCESS TIME (u Sec)	HARDWARE PATH OF RETRIEVED DATA	INTERFACE INTERFERENCE	TYPICAL TYPES OF DATA THAT WOULD BE RETRIEVED (From Table 1)
1	ADAGE MEMORY	8.1 or $\pm 15.6$	AM $\rightarrow$ AM	NONE	TARGET DATA (1B) GYRO, SPEED (2)
2	XDS ACCESSABLE MEMORY	370 $+8(n-1)$	AM $\rightarrow$ IB $\rightarrow$ XMA $\rightarrow$ IB $\rightarrow$ AM	INTERRUPTS XDS HARDWARE	INTERCEPT POINT (4A) LAUNCHER ANGLE ORDERED (4E)
3	XDS INACC. MEMORY	920 $+25(n-1)$	AM $\rightarrow$ IE $\rightarrow$ XMA $\rightarrow$ XP $\rightarrow$ XMI $\rightarrow$ XMA $\rightarrow$ IB $\rightarrow$ AM	INTERRUPTS XDS HARDWARE AND SOFTWARE	LAUNCHER DATA SAMPLE RATE (4F) LAUNCHER BEARING LIMITS (4G)
4	ALTERNATE ADAGE MEMORY	1.95 msec $+30(n-1)$	AM $\rightarrow$ IB $\rightarrow$ XMA $\rightarrow$ XP $\rightarrow$ IB $\rightarrow$ AM <sub>2</sub> $\rightarrow$ IB $\rightarrow$ XP $\rightarrow$ XMA $\rightarrow$ IB $\rightarrow$ AM <sub>1</sub>	INTERRUPTS XDS HARDWARE TWICE, ADAGE PROGRAM ONCE	TARGET DATA (3B) TOTAL SYSTEM CONFIGURATION (3C) (SEE FIGURE 5)
5	FIXED ANALOG DATA	1.0 msec $+50(n-1)$	AM $\rightarrow$ IB $\rightarrow$ XMA $\rightarrow$ XP $\rightarrow$ HI $\rightarrow$ AC $\rightarrow$ HI $\rightarrow$ XP $\rightarrow$ XMA $\rightarrow$ IE $\rightarrow$ AM	INTERRUPTS XDS PROGRAM TWICE	BEARING (5A) ELEVATION (5B)
6	RANDOM ANALOG DATA	18 m sec	AM $\rightarrow$ IB $\rightarrow$ XMA $\rightarrow$ XP $\rightarrow$ HI $\rightarrow$ AC (SP) $\rightarrow$ HI $\rightarrow$ XP $\rightarrow$ XMA $\rightarrow$ IE $\rightarrow$ AM	INTERRUPTS XDS PROGRAM TWICE AND ANALOG simulation once	ERRORS IN BEARING (5C1) DRIFT RATES (5C2)



#### b. A Specific Example

Figure 5 describes a specific example of retrieving data from a specific computer. Note the complex path that is necessary to access this data, ten data transfers in all. This is to be expected in large real-time systems and must be timed accurately. The data must first be requested from the data sampling program located in Adage 1. This request passes through the XDS-9300 computer to the Adage 2 computer. The Adage 2 computer must then access the requested data and pass it back to the Adage 1 computer via the XDS-9300 computer. While this data path is long, the majority of the time required is for program initiations that must be set-up (1.95 m sec). Thereafter, only a short time (30  $\mu$  sec) is required for each additional word retrieved, i.e., 100 words may be transferred in 3 m sec.

#### 4. Monitor Program Module

The Monitor Program contains all items for Fault Monitoring as discussed. This program is divided into three related segments: (1) Program timing analysis and priority setup, (2) Data sampling and (3) Human operator interface. Each segment is independent, but relies upon completion of related tasks. Upon completion of all tasks, the cycle of monitoring the total system is complete.

##### a. Timing Analysis

This subprogram samples the system usage of time to allow the most efficient allocation among the required tasks. Any time that remains after the required tasks have been completed is surplus time. In most systems, this surplus time is not utilized. For example,

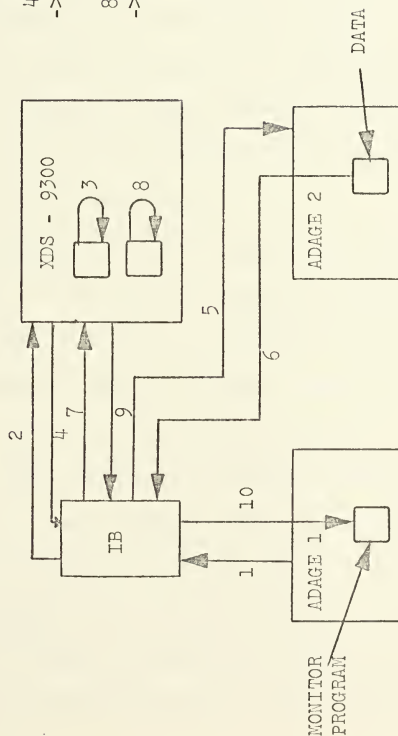


FIGURE 5

EXAMPLE OF DATA SAMPLING (Level 4)

1. To retrieve the data called "Total system configuration" (3C)
2. The data is required in the monitor program (located in Adage 1); it must be retrieved from the CIC data store (located in Adage 2).
3. The data flow commences as follows:

1	2	3
AM <sub>1</sub> ->	IB ->	XMA ->
4	5	6
->	IB ->	AM <sub>2</sub> ->
7		
	8	9
		10
	->	XMA ->
		IB ->
		AM <sub>1</sub>



TIME REQUIRED TO ACCESS, 1950  $\mu$  SECONDS.





if the timing analysis program detects three milliseconds of time left in a ten millisecond executive loop, it allocates as much of the three milliseconds to Fault Monitoring as is possible. The allocation of this time is used in three different modes.

(1) Light loading mode. When the computer system is only lightly loaded (say 30%), many fault monitoring tasks may be accomplished. With this amount of time available, many monitoring tasks normally accomplished under maintenance down time may be loaded by segments into the computer memory from the disc by the monitor program. Since the probability of this lightly loaded condition occurring for a reasonable amount of time is high, many time slots allotted to the fault monitor may be utilized in loading fault detection and diagnostic programs for later execution. Execution of these programs in addition to those discussed below maintains the computer system at its greatest reliability.

(2) Medium loading. When the computer system is at moderate loading (say 60%), little program swapping of monitor routines is allowed. All major system functions are monitored and routine maintenance tests are performed only periodically, a section at a time. By concentrating the monitoring function on detecting errors of major system functions, the up time reliability is greatly enhanced by insuring system operation. When an error is detected, a quick system reconfiguration reduces the Mean Time to Repair (MTTR) to near zero.

(3) Heavy loading. When a computer system is heavily loaded every millisecond is needed to maintain the system in operation. This is system utilization of about 90 percent. With such little time



available, most real-time multi-processor systems accomplish no software fault monitoring at all. The results is that the smallest error can cause the total system to fail. This is a very bad mistake! It is in this situation that on-line Fault Monitoring is needed the most. By monitoring only the most critical data points and completing this task over many time slices, an effective monitoring program can be carried out even under heavy loading. Timing analysis is most important when there is very little time available. The process of dynamic time allocation can be shown to be most effective for the heavily loaded system.<sup>[18]</sup> When a serious permanent fault occurs, more time may be utilized for diagnostic routines to accurately locate the fault. With the imminent prospect of system failure, the locating and correcting of the fault now has highest priority. Only very perishable data need be saved so that an operable system may be restored on system restart. Therefore, a computer system with adequate fault monitoring will have greatly enhanced system reliability even during critical periods of heavy loading.

#### b. Data Sampling

The data that is needed for fault monitoring is sampled through the data paths and stored efficiently in core or disc. Redundant data is filtered and only data permutations are actually stored. This process requires an intricate scheme for storing data since the core space and the time available are both critical. For example, the azimuth angular rotation of the missile mount is considered to be continuous. Rather than store all data points over several seconds (about 500 points), only one data point need be saved.



This would be the "old" azimuth angle and would be compared to the newly acquired angle. The difference would then be compared against a maximum allowed difference. Whenever this maximum difference was exceeded, an error would be generated. Thus only three words ("old" angle, "new" angle, and maximum difference) must be stored compared to possibly 500.

All data samples needed for adequate fault monitoring are grouped into sections, only those subprograms required are brought into core and executed. For example, the data elements (table 1) needed to be monitored for fault detection and the related extraction times (from table 5) are shown below.

Data	Access Time
1. Radar azimuth	8.1 $\mu$ sec
2. Radar target range	8.1 $\mu$ sec
3. Radar target bearing	8.1 $\mu$ sec
4. Ship latitude	8.1 $\mu$ sec
5. Ship longitude	8.1 $\mu$ sec
6. Missile intercept point	370 $\mu$ sec
7. Missile time to fire	370 $\mu$ sec
8. Missile target destruction evaluation	370 $\mu$ sec
9. Missile mount bearing	370 $\mu$ sec
10. Missile mount elevation	370 $\mu$ sec

Each data point can be retrieved individually or as a group. The access time of an individual data item is different than that of a group. The best method to choose is the method that results in the smallest average access time per data element. For example, by



summing the individual access time for data items one through five, we obtain 40.5  $\mu$  seconds. Table 5 shows that if more than one data element of this type is accessed, the time of access is 15.6  $\mu$  seconds. Therefore by accessing data items one through five as a group, the required access time is 78.0  $\mu$  seconds. In this example, it is more advantageous to access each data item individually than by a group. Similarly summing the individual access time of items six through ten, we obtain 1,850  $\mu$  seconds. Again using table 5, we find that these same data elements accessed as a group require only 402  $\mu$  seconds. In this example, the data access time of a group is much less than that of the same items retrieved individually.

### c. Human Operator Interface

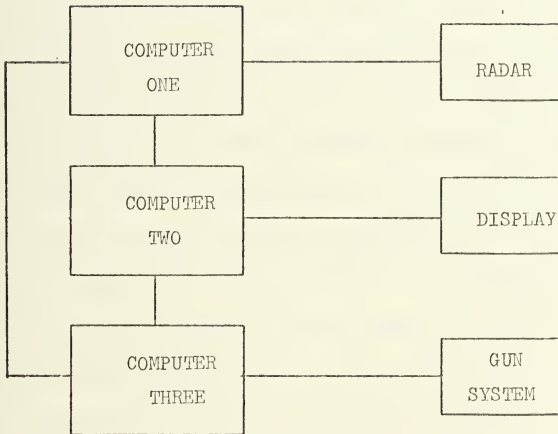
The human operator interface module accepts the sampled data and analyzes the data for faults. Upon detecting a fault, the pertinent data is displayed to the operator as an alert. Requests from the operator are input into this module and displayed in the proper format. All human input actions, such as a light pen hit or function switch depressed are recognized by this program module and acted upon.

If reconfiguration is requested by the system monitor operator, a system study is conducted by the reconfiguration program module to analyze the current configuration. Then the program looks up the entry in the reconfiguration table appropriate to the component which has failed and displays the recommended reconfiguration for operator approval. (see fig. 6) If the operator approves this reconfiguration, he presses a function switch labeled "accept" and the program continues and executes the recommended reconfiguration.





PROPOSED RECONFIGURATION



SEMIAUTOMATIC MODE

IF ACCEPTABLE PRESS ACCEPT

IF NOT ACCEPTABLE MODIFY AS REQUIRED

FIGURE 6



If the system monitor operator disapproves of the recommended re-configuration, he may alter the display by an appropriate manner and then order the computer to execute this new configuration.

#### 5. Combat Information Center Program (SMULA)

A simulation of a Ship Combat Weapon System was programmed on the two Adage Graphic Terminals available at the Naval Postgraduate School. (see Program A and B) Three systems were simulated: (1) Combat Information Center, (2) Radar and (3) Ship Information.

The basic purpose of this simulation was to provide a model on which to test the ideas presented in the preceding section for a fault monitoring system. The simulation provides an actual model of combat between a ship and an aircraft. A display of the position of the ship and the airplane has been incorporated to allow visual following of the action. The simulation is programmed for the airplane to approach and attack the ship, firing missiles at the ship when close enough. The ship in turn must detect the airplane, and make the decisions of hostility, of time to fire and of target destruction.

##### a. Main Control and Combat Information Display (CICP)

This program operates in one Adage Graphic Display terminal (see Program A ). The timing of the overall simulation is controlled in this module.

The command and control systems purpose is to accept data from the radar simulation and make a decision upon the identity of the target. If it is identified as hostile, a "kill" order is sent to the Fire Control Computer System Module. Since the time required for the decision process has a Poisson Distribution, it is simulated



by an constant eight second delay plus a random delay from an exponential random number generator with an expectation of four seconds.

As the fire control module rotates the missile launcher, the missile launcher displayed on the ship moves in synchronism with the missile launcher simulation on the Concor analog computer. After lock-on to the target, the combat system "launches" a salvo of two missiles. These are simulated on the Adage display as a pair of bright dots, one after the other, originating from the missile launcher and moving towards the target. If the missiles "hit" the target, the target explodes into many bits, simulated by many dots randomly spaced. If the plane launches a missile and "hits" the ship, the ship explodes in the same way. All missiles are simulated as nuclear type.

b. Radar and Ship Information Simulator (RADAR)

This program simulates the radar and ship information systems on the ship. It is located in one Adage Graphic Terminal. (see Program B)

The radar's purpose is to detect all incoming targets as soon as possible and relay information on range and bearing to the Combat System. For the simulated radar, a maximum range of one hundred miles was chosen. The delay between the time that the approaching aircraft crosses the point of maximum range and the time that the target data is actually transmitted, has a uniform random distribution with a maximum of eight seconds and a minimum of four seconds. Eight seconds was chosen after considering the antenna rotational speed and the number of rotations required for the radar operator to confirm an actual target.



The Ship Information subprogram simulates the ship as moving on a steady course at a speed of thirty knots. This information is passed to the CICIP program and is used to move the simulated ship display.

The radar simulator has a model of a simulated airplane. The airplane model moves at a speed of 3,600 knots. The airplane starts on a course of  $270^{\circ}$  T. When it is within 100 miles of the ship, it turns to automatically attack the ship. Manual override controls are provided to control the course, altitude and missile firing. These are controlled by function switches adjacent to the display.

#### 6. Fire Control System Program (Missile Mount)

A simulation of a Digital Fire Control System and of a Missile launching Mount was programmed on the Xerox Data Systems (XDS) 9300 digital computer and the Comcor CI-5000 analog computer. The Digital Fire Control System was simulated on the XDS-9300 computer (see Program C). The Missile Launching Mount was simulated on the CI-5000 analog computer (see Program D).

The basic purpose of these simulations was to provide a Digital Fire Control System and a Missile Mount to interact with the simulated Ship Combat Weapon System on the Adage Graphic Terminals. The Digital Fire Control System was written in FORTRAN IV on the XDS-9300 computer and uses its hybrid capabilities to communicate with the CI-5000 analog computer. The Missile Mount is simulated to act like a real mechanical-electrical missile mount. Upon assignment of a target azimuth, the missile mount moves as a missile mount aboard ship would move.





#### a. Digital Fire Control System

The digital fire control system accepts target information from the Combat System and converts this rectangular coordinate data to polar coordinates for the fire control missile mount. It must then order the missile mount to move from its present azimuth to the target azimuth. In a total analog system, this would be all that would be required; the analog feedback system would effect the required movement. In a digital system many improvements may be gained. Overshoot and time to rotate can be minimized under digital control. The digital control utilizes a modified "Bang-Bang" approach that uses six phases. Each phase implements a separate portion of the task of moving the missile mount. With appropriate programming, the missile mount moves at the fastest speed possible with the smallest overshoot. Digital Control optimizes the control of this simulated large and massive Missile Mount as it does in the real case.

#### b. Missile Launcher Mount

The missile mount was simulated on the CI-5000 analog computer using hybrid computer techniques. The simulated mount consists of an amplidyne controlled generator that drives a large motor which in turn drives a 100:1 gear train connected to the missile mount. The amplidyne requires 33 volts per field ampere and in turn controls the field coil of the generator that can produce 25 amperes at 440 volts. The generator drives a 200 horsepower motor at speeds up to 1150 RPM. The weight of the mount is 28 tons and may rotate at a rate of up to one radian per second. The resulting transfer and analog computer equations of the fourth order system are:



$$\text{I} \quad \ddot{e}_{gf} = -112 \dot{e}_{gf} - 3030 e_{gf} + 3.35 (10^4) e_{af}$$

$$\text{II} \quad \dot{i}_{gf} = -.3333 I_{gf} + .0333 E_{gf}$$

$$\text{III} \quad I_a = 87 I_{gf} - 7.67 \dot{\theta}_m$$

$$\text{IV} \quad \ddot{\theta}_m = .032 I_a - .0909 \dot{\theta}_m$$

$$\text{V} \quad E_{af} = K ( \theta_r - \theta_l )$$

Where	$g$ - generator	$m$ - motor
	$l$ - launcher	$f$ - field
	$a$ - armature	$K$ - constant

Both azimuth and elevation controls are implemented on the analog computer and have been verified to be similar to a shipboard missile mount. The analog simulation adds realism to the combat system and allows actual hardware items to be monitored by the fault monitoring system.



## V. RECOMMENDED TECHNIQUES

By analyzing the process used in fault monitoring in the simulated Ship Combat Weapon System, the overall technique should now be clear. By applying the following techniques, a Real-Time Multi-Computer Monitoring system may be designed to operate effectively even under heavy loading conditions.

### A. DYNAMIC TIME ALLOCATION

The hardware and software data transfer rate between all hardware components must be accurately determined by an interface timing study. This may be accomplished by writing a simple program loop passing data between the components. All critical data (critical to the hardware and software partitions) must be determined and listed as either hardware or software accessible data needed for data evaluation. From the interface timing study, the time required to access this data may be determined. From this data list, groups of data should be determined so as to best fit the minimum time allotted to fault detection under heavily loaded conditions. This grouping must be done in conjunction with the study of Partitioning. When the final list is completed and all groupings made, this data becomes the basis of the data sampling program module.

The timing analysis program module works directly with the data sampling program. By analyzing the system resources, time allocation may be distributed to a number of data sampling group subprograms and data analysis programs. For example, two milliseconds may be allocated to monitor all critical data points of the radar system.



## B. PARTITIONING

The multi-computer system must be partitioned into hardware and software logical subelements. By determining the degree of reconfiguration possible, e.g., the number of CPU's, the degree of partitioning becomes known in part because partitioning and reconfiguration are interrelated. Both must be determined together in order to optimize system resources. If two partitioned elements may not be used interchangeably for reconfiguration, then the partitioning is too small. Partitioning must also consider what data inside a proposed subelement is critical. Normally each logical element of a multi-computer system has a number of data elements that can be used to determine when the logical element has failed. These data elements are the critical data points of this partitioned logical subelement.

If a computer program is critical to the operation of a multi-computer system and has no replacement, then a simulation of the program should be included in the system. A simulation of a program may be a smaller version of the replaced program or it may be a dummy program that allows the total system to remain operational at a reduced level. Then a software fault in this program, that can not be corrected by the relocation of the program, may be corrected temporarily by the use of the simulation. While the simulation is maintaining the system at a degraded level, the error in the program may be corrected and the program then reinstated into the system.

After the system has been satisfactorily partitioned, the subsystem elements become the system status list. The logical connections (I/O) of these partitions are then also fixed and inserted into tables in the reconfiguration program module.





### C. FAULT RECOGNITION

Since the critical data has been determined under the study of Dynamic Time Allocation, only the method of fault recognition remains. Software errors are only recognized by software routines, but hardware faults are best detected by a combination of hardware and software. If hardware devices are present to detect the faults, then they should be used in preference to software routines as hardware detection is much faster. If some faults require an exorbitant amount of time to be recognized in software, then the use of special purpose hardware registers and fault detectors should be studied. Special purpose hardware fault detectors operate at a much higher speed but may be more expensive than software subroutines.

Permanent faults and errors may be detected and analyzed by hardware or software, but transient faults and errors may only be economically analyzed by software routines. Provisions for detecting and analyzing transient errors must be included in the system.

### D. DIAGNOSTIC ROUTINES

Diagnostic routines become smaller in fault monitoring programs that recognize faults at the subsystem level. Normally a fault may be due to any one of hundreds of likely components. All components must be diagnosed to determine which component is at fault. Since any subsystem may only have from three to five critical data points (for example), the diagnostic routine necessary to locate a subsystem error may be simpler in design.

Normal diagnostic routines, used to diagnose computers and special hardware devices, are utilized in this fault monitoring system also.



Since most of these routines require run times of minutes, they must be segmented into logical time elements that may be called by the system dynamic time allocation routine and executed when the computer is lightly loaded. In this way complete diagnostic analysis of the total multi-computer system can be accomplished.

#### E. SYSTEM RECONFIGURATION AND PRESENTATION

Items A-D above compile all the necessary data for system reconfiguration and presentation to the System Monitor Operator. The system status list that was generated from the study of Partitioning may now be used to determine when and how a system might be reconfigured. A set of possible configuration lists or even a program that computes an acceptable reconfiguration is available for the use of the reconfiguration program. When requested, the program studies the submodule that has failed to see if it is on the current subsystem active list. (see fig. 7 for a sample list) If it is, it removes it and places it on the non-active list. The program searches the possible configuration lists until it finds a match with the current subsystem active list. If a match is not found, it notifies the system monitor operator and halts. Before continuing, if the semi-automatic mode is set, the reconfiguration is presented to the operator for approval. In the automatic mode, this step is omitted. By resetting the logical interface list, any faulty input/output ports are bypassed. If needed, a program may be relocated (by reloading it). If the fault is programmatic, a suitable simulation program may be loaded to replace the faulty one.

The type of presentation displayed to the monitor operator depends upon the equipment available and the system being monitored. Since



# EXAMPLE OF A SUBSYSTEM ACTIVE LIST

ACTIVE	COMPONENT (see fig. 3)
yes	RADAR
yes	SHIP DATA
yes	MONITOR
yes	CPU #1
yes	CORE 1a
yes	CORE 1b
yes	CORE 1c
no	CORE 1d
yes	DISPLAY 1
yes	INTERFACE 1
yes	INPUT DATA
yes	OUTPUT DATA
yes	CPU #2
yes	CORE 2a
yes	CORE 2b
no	CORE 2c
no	CORE 2d
no	DISPLAY 2
yes	INTERFACE 2
yes	DAC
yes	ADC
.	.
.	.
etc.	etc.

Figure 7



the data to be presented is voluminous and time critical, any mechanical device would be too slow. Some type of Cathode Ray Tube (CRT) with function switches and maybe a typewriter input is needed. Then the fault, its location and recommended solutions can be displayed simultaneously. The recommended reconfiguration presentation can be either displayed as a logic diagram showing the reconfigured components and their links (see fig. 6) or the two lists (old and new reconfiguration lists) can be displayed side by side. Because of the rapid visual assimilation by the operator of the displayed data, rapid decisions may be made.





1.1  
1.2  
1.3  
1.4  
1.5  
1.6  
1.7  
1.10  
1.11  
1.12  
1.13  
1.14  
1.15  
1.16  
1.17  
1.20  
1.21  
1.22  
1.23  
1.24  
1.25  
1.26  
1.27  
1.30  
1.31  
1.32

COMPUTER PROGRAM A  
MAIN CONTROL AND COMBAT INFORMATION DISPLAY

(CICP)

```

*****
[ THIS PROGRAM SIMULATES A REAL TIME SYSTEM,
[ A DLGN-36, USS CALIFORNIA, AND A F-111 FIGHTER.
[ THE SIMULATION MODELS A HIGH SPEED ATTACK ON THE
[ SHIP, THE PLANE DETECTS THE SHIP AT 40 MILES
[ AND RELEASES AN ANTI SHIP MISSILE AT 20 MILES.
[ THE SHIP DETECTS THE PLANE AT 100 MILES AND
[ RELEASES 2 ADVANCED TERRIER MISSILES AS S99N
[ AS POSSIBLE WITH THE DETERMINED DELAYS.
[ ALL SYSTEMS ARE SIMULATED WITH THE DELAYS
[ BEING CALCULATED BY THE COMPUTER.
[
[ H.D.K. 8/12/69
[ D.A.T. UBC 10/20/69
[ E. A. PATER 1/9/70 Y8D 70-11-9
*****

```







3.1	MDAR'L'A		
3.2	77777JH		
3.3	ARMD DAT		
3.4	MDAR DY		
3.5	15420VH PS		
3.6	N80P		
3.7	17440VH 15.	[ARRS 15.	
3.10	N80P		
3.11	MDAR'L'A		
3.12	77777		
3.13	MDAR'9 DAT		
3.14	67100VH		
3.15	MD10'A AVG8F	[ST9P CLCK	
3.16	MD10'9 AVGN	[START AVG	
3.17	MDIR E9VPV		
3.20	ENDM		
3.21	MD05=25000VH; MD07=27000VH; MD10=30000VH		
3.22	SPL1=1; SPR1=5; SPH1=9.; SPF1=13.; SPI1=0		
3.23	SPL2=16.; SPR2=12.; SPH2=8.; SPF2=4; SPT2=-1		
3.24	[CLKEN = CLCK SETTING, ARGPV = 9VERFLGW PIV9T		
3.25	CLKEN=1000VH; FRMPV=77755; E9VPV=77756; E9LPV=77757; ARGPV=77771		
3.26	THRST: 14	[THRUST	
3.27	TVC: 4	[	
3.30	HYPR: 120.	[HYPER TIME A	
3.31	TTIME: 300.	[TRPED9 TIME	
3.32	PRFS: 1000	[POINT 9F FIRE START	
3.33	TVFL: 110	[TRPED9 VELOCITY	
3.34	HYPR8: 120.	[HYPER TIME B	
3.35	EXP: 60.	[TIME FOR EXPLSION	
3.36	GENE: 200.	[TIME T9 BE GENE	
3.37	TPCT: 32.	[TRPED9 COUNT	
3.40	SUNR: 600	[SUN RADIUS	
3.41	SHPCR: 25	[SHIP CIRCLE FOR COLISION	
3.42	TSHCR: 770	[TRPED9 T9 SHIP CIRCLE RADIUS	
3.43	TSHC2: 500		



[TORPEDS TO TORPEDS RADIUS  
 [SCALE FACTOR FOR SHIPS  
 [POSITIVE FULL BRIGHTNESS  
 [YAW RATE  
 [STARS ON FLAG

[TIME OF RELEASE OF TORPS  
 [AMOUNT OF FUEL  
 [SIZE FOR EXPLOSION  
 [AMOUNT OF GRAVITY

[TWO LISTS 1 AND 2

[1 POINT BUFFERS EACH  
 [8 RANDOM NUMBERS EACH

TCCR: 500  
 SCL: 2000  
 PS: 23000  
 YAW: 40  
 NSTRS: -1  
 STRXM: 10  
 SUNBM: 0  
 RLI: 200  
 FUEL: 2400.  
 TEXP: 6000  
 KGRAV: 100  
 HYRT: 120.  
 DAT: 0  
 IREPEAT 0,(1,2)  
 RLTAQ: 0  
 GONEAQ: 0  
 EXPAQ: 0  
 HYPAQ: 0  
 HYBPAQ: 0  
 XACCAQ: 0  
 YACCAQ: 0  
 FUELAQ: -0  
 TVCAQ: -0  
 XVELAQ: 0  
 YVELAQ: 0  
 XGRVAQ: 0  
 YGRVAQ: 0  
 HYRTAQ: 0  
 TPCTAQ: 0  
 SHFAQ: 0  
 SHQAQCR: 0  
 QNPTBF: L9C .+100.  
 RNDLAQ: REPEAT 8, 0;  
 ENDR 18  
 ENDI  
 BAX: 07000JH 54000





5.1	07000JH 54000	
5.2	37776JH 54001	
5.3	37776JH 40001	
5.4	07000JH 40001	
5.5	07000JH 54001	
5.6	07001JH 54001	
5.7	TERPS: REPEAT 91., 1JH: ENDR	
5.10	TERPA: LOC. +140	[STORAGE FOR TORPED9ES
5.11	YAW1: 20000	
5.12	YAW2: 60000	
5.13	XPOS1: 60000	[STARTING DATA FOR FIGURES
5.14	YPOS1: 60000	
5.15	XPOS2: 57777	
5.16	YPOS2: 57777	
5.17	INIT: 0	
5.20	DONEF: 0	
5.21	STRIF: 0	[INIT -0
5.22	SUN9: -0	
5.23	DISDN: -0	
5.24	STPTR: STARS-1	[STAR P9INTER
5.25	FIA: 0	[INIT TERPA +134
5.26	TLA: 0	
5.27	TEM1: 0	[INIT TORPA +4
5.30	TEM2: 0	
5.31	TPTR: 0	
5.32	AROVF: 0	[AR OVERFLOW STORAGE
5.33	SSCL: 0	
5.34	PTR1: 0	
5.35	PIR2: 0	[TORPED9 POINTERS
5.36	PIF3: 0	
5.37	PIR4: 0	
5.40	PIR6: 0	
5.41	PIR7: 0	
5.42	PIR8: 0	
5.43	LPTR: 0	



6.1	SPTR: 0	[INIT TORPS -1
6.2	PREV: 0	
6.3	SWITH: 0	[SWITCH H
6.4	SWITL: 0	[SWITCH L
6.5	NSTR: 0	[N9 STARS
6.6	SDFLG: 0	
6.7	BRCNT: 0	
6.10	STTEM: 0	
6.11	B9XF: 0	[B9X FLAG
6.12	GRVF: 0	[GRAVITY FLAG
6.13	STRF: 0	
6.14	KILSN: 0	
6.15	ZER0: 0	
6.16	AMASK: 77777	
6.17	ONE: 01: 1	
6.20	E0LBIT: 1/H 0	
6.21	CM11: -(1/H 1)	
6.22	SEVEN: 7	
6.23	C11: 1/H 1	
6.24	MZRO: -0	
6.25	HMZRO: 77777H	
6.26	SHAGF: -20/H	
6.27	FS: 37777	[DISPLAY SCALE
6.30	HFS: 20000	
6.31	AVG0F: CLK9N 101000	
6.32	AVG8N: 60740/H	
6.33	RANDM: JUMP •	[SIMPLE RANDOM NUMBER GENERATOR
6.34	MDAR'H RNDN	
6.35	MDAE'F'B 53110	
6.36	MDX9 •-1	
6.37	ARM0 RNDN	
6.40	MDIR RANDM	
6.41	RNDN: 0	
6.42	RNDM2: JUMP •	[THREE STEP RANDOM N9 GEN
6.43	JPSR RANDM	



7.1	17500JH 5	
7.2	MDAR'A'I RNDM2	
7.3	ARM D RNDT	
7.4	JPSR RANDM	
7.5	JPSR RANDM	
7.6	17500JH 7	
7.7	MDAR'A'I RNDM2	
7.10	MDAE RNDT	
7.11	MDAE'I'N RNDM2	
7.12	MDIR'X RNDM2	
7.13	RNDT: 0	
7.14	AROSB: JUMP • [FOR OVERFLOW	
7.15	ARM D'0 ARGVF	
7.16	MDIR ARSB	
7.17	NULL: JUMP • [FOR INDERRECT JUMP	
7.20	JUMP'I NULL	
7.21	CLOCK: JUMP • [UPDATES CLOCK	
7.22	ARM D EGLAR	
7.23	MDAR'H'X DONEF	
7.24	JPAN ,+2	
7.25	ARM D'0 STRTF	
7.26	MDAR EGLAR	
7.27	JUMP'I CLOCK	
7.30	EGLAR: 0	
7.31	EGL: JUMP •	
7.32	ARM D'0 DISDN	
7.33	JUMP'I EGL	
7.34	CGRV: JUMP • [CALCULATES GRAVITY	
7.35	MDIR CGRAV	
7.36	R0TATE: JUMP •	
7.37	ARM D XYR0TATE	
7.40	MDAR'A'1	
7.41	77777	
7.42	ARM D YR0TATE	
7.43	MDAR'H XYR0TATE	

[CALCULATES ROTATION



10.1  
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MDAR'L'A  
77777  
ARMD XRGTATE  
15420VH C6SN  
N8CP  
ARMD TEMRGATE  
MDAR'N SINE  
15420VH YRGATE  
N8CP  
MDAE TEMRGATE  
MDAR'A'L  
77777VH O  
ARMD TEMRGATE  
MDAR YRGATE  
15420VH C6SN  
N8CP  
ARMD YRGATE  
MDAR XRGTATE  
15420VH SINE  
N8CP  
MDAE YRGATE  
17440VH 15.  
N8CP  
MDAR'L'A  
77777  
MDAR'0 TEMRGATE  
MDAR'A'L  
77776VH 77776  
ARMD TEMRGATE  
MDAR'L  
1VH 1  
MDAR'A XYRGATE  
MDAR'0 TEMRGATE  
MDIR RGTATE  
TEMRGATE: O





11.1	XRSTATE: 0		
11.2	YRSTATE: 0		
11.3	XYRGSTATE: 0		
11.4	CICP: JUMP •	ENTRY POINT	
11.5	ARXØ'F		
11.6	ARMØ'N STRF		CT9 DELETE STARS AND GRAVITY
11.7	ARMØ'N GRVF		
11.10	MDAR'F DATASTORE		
11.11	ARMØ DATASTORE		
11.12	MDAR'I CICP		END ØF ADDITION
11.13	JPLS •+2		
11.14	JUMP INITL-4		
11.15	ARMØ THRST		
11.16	MDAR'I'X CICP		
11.17	ARMØ TVEL		
11.20	MDAR'X'I CICP		
11.21	ARMØ KGRAV		
11.22	MDAR'L: 57777	SET INITIAL POSITION ØF SHIP	
11.23	ARMØ XØS2		
11.24	ARMØ YØS2		
11.25	INITL: MDAR'F NULL		START ØF INITIALIZATION
11.26	ARMØ FRMPV		STARTS CLOCK
11.27	MD10 ZERO		
11.30	JPSR STØVER		TRANSFERS INIT IF SET
11.31	MDAR'F EØL		
11.32	ARMØ EØLPV		
11.33	MDAR'L		
11.34	JPSR ARØSB		
11.35	ARMØ ARØPV		
11.36	MDAR'F STARS		
11.37	ARMØ'L		
11.40	STARSPTR: 0		
11.41	STARSGEN: JPSR RANDM		
11.42	MDAR'A CM11		
11.43	ARMØ'X'I STARSPTR		



12.1 ARMD'X'I STARSPT  
 12.2 MDAR'0 ONE  
 12.3 ARMD'X'I STARSPT  
 12.4 MDAR'F STARSND  
 12.5 MDAR'N STARSPT  
 12.6 JPN +2  
 12.7 JPN STARSND  
 12.10 MDAR STARS2-1  
 12.11 MDAR'0 EOLBIT  
 12.12 ARMD STARS2-1  
 12.13 MDAR STARS3-1  
 12.14 MDAR'0 EOLBIT [ADDS EOL BIT  
 12.15 ARMD STARS3-1  
 12.16 MDAR STARSND-1  
 12.17 MDAR'0 EOLBIT  
 12.20 ARMD STARSND-1  
 12.21 ARMD'F  
 12.22 ARMD RLT1  
 12.23 ARMD GEN1  
 12.24 ARMD EXP1  
 12.25 ARMD HYP1  
 12.26 ARMD HYPB1  
 12.27 ARMD XVEL1  
 12.30 ARMD YVEL1  
 12.31 ARMD XGRV1  
 12.32 ARMD YGRV1  
 12.33 ARMD HRT1  
 12.34 ARMD RNDL1  
 12.35 ARMD RNDL1+1  
 12.36 ARMD RNDL1+2  
 12.37 ARMD RNDL1+3  
 12.40 ARMD RNDL1+4  
 12.41 ARMD RNDL1+5  
 12.42 ARMD RNDL1+6  
 12.43 ARMD RNDL1+7

[SETS CORE TO ZERO



13.1	ARM'D'9 TVC1
13.2	ARM'D RLT2
13.3	ARM'D GNE2
13.4	ARM'D EXP2
13.5	ARM'D HYP2
13.6	ARM'D HYPB2
13.7	ARM'D XVEL2
13.10	ARM'D YVEL2
13.11	ARM'D XGRV2
13.12	ARM'D YGRV2
13.13	ARM'D HRT2
13.14	ARM'D RNDL2
13.15	ARM'D RNDL2+1
13.16	ARM'D RNDL2+2
13.17	ARM'D RNDL2+3
13.20	ARM'D RNDL2+4
13.21	ARM'D RNDL2+5
13.22	ARM'D RNDL2+6
13.23	ARM'D RNDL2+7
13.24	ARM'D TCGUNT
13.25	ARM'D THE12F
13.26	ARM'D TGR22F
13.27	ARM'D FCSEF
13.30	ARM'D SIMF2
13.31	ARM'D SIMF3
13.32	ARM'D SIMF4
13.33	ARM'D SIMF5
13.34	ARM'D SIMF6
13.35	ARM'D SIMF7
13.36	ARM'D SIMF8
13.37	ARM'D SIMF9
13.40	ARM'D SIMF10
13.41	ARM'D SIMF11
13.42	ARM'D SIMF12
13.43	ARM'D SIMF13



14.1	ARMD SIMF14	
14.2	ARMD SIMF15	
14.3	ARMD SIMF16	
14.4	ARMD SIMF17	
14.5	ARMD SIMF18	
14.6	ARMD DATATEMP	
14.7	ARMD'G TVC2	
14.10	ARMD INIT	
14.11	ARMD IBLK	
14.12	ARMD TLA	
14.13	ARMD TORPA	
14.14	ARMD STRIF	
14.15	ARMD'G DISDN	
14.16	ARMD'G SUNB	
14.17	MDAR'L	
14.20	20000	
14.21	ARMD YAW2	
14.22	MDAR'L; 40000	
14.23	ARMD YAW1	
14.24	MDAR'L; 37777	
14.25	ARMD XPOS1	
14.26	MDAR'L; 77777	
14.27	ARMD YPOS1	
14.30	MDAR'L; -20	
14.31	ARMD XVEL1	
14.32	MDAR'L; 1	
14.33	ARMD YVEL2	
14.34	ARMD SIMF1	
14.35	MDAR'L; 3000	
14.36	ARMD THETA	
14.37	MDAR SIMTEN	
14.40	ARMD'N SIMTIME	
14.41	MDAR'N TPCT	
14.42	ARMD TPCT1	
14.43	ARMD TPCT2	

[SET TO ZERO FOR AGT2

[ADDED





15.1 MDAR'N FUEL  
 15.2 ARMD FUEL1  
 15.3 ARMD FUEL2  
 15.4 MDAR'L  
 15.5 TERPA+4  
 15.6 ARMD TEM1  
 15.7 INI1: MDAR TEM1  
 15.10 MDAS'F'N 4  
 15.11 ARMD'I TEM1  
 15.12 MDAR TEM1  
 15.13 MDAS'F 4  
 15.14 ARMD TEM1  
 15.15 MDX0'F TERPA+140  
 15.16 JPLS INI1  
 15.17 MDAR'L  
 15.20 TERPA+134  
 15.21 ARMD FTA  
 15.22 MDAR'F TLA  
 15.23 ARMD LPTR  
 15.24 MDAR'F CL9CK  
 15.25 ARMD FRMPV  
 15.26  
 15.27 MDAR'H'X NAMEC  
 15.30 JPAN STN4  
 15.31 MDAR'L: -400  
 15.32 ARMD DONEF  
 15.33 MDAR -2  
 15.34 ARMD NAMEC  
 15.35 STN2: JPSR COFF  
 15.36 JPSR SUP  
 15.37 STNAM  
 15.40 STNAM  
 15.41 NAME  
 15.42 MDAR'H'X NAMEC  
 15.43 JPAN STN2

INITIAL 1







17.1	ARXB'F		
17.2	ARMD STRTF		
17.3	MDAR SWITH		
17.4	ARMD SWITL		
17.5	MDIC'0'L		
17.6	4GVH		
17.7	SSAR'F'H		
17.10	17500VH 1		
17.11	MDAR'L'A		
17.12	1VH 77776		
17.13	ARMD SWITH		
17.14	MDIC'A'L		
17.15	-40VH		
17.16	SSMD TEM1		
17.17	MDAR'H TEM1		
17.20	MDAR'A ONE		
17.21	MDAR'0 SWITH		
17.22	ARMD SWITH		
17.23	MDAR TEM1		
17.24	MDAR'A'H'L		
17.25	6		
17.26	MDAR'0 SWITH		
17.27	ARMD SWITH		
17.30	MDAR SWITL		
17.31	MDXB'A SWITH		
17.32	ARMD TEM2		
17.33	MDAR'H'N TEM2		
17.34	17500VH 4		
17.35	JPAN *+3		
17.36	MDAR B0XF		
17.37	ARMD'N B0XF		
17.40	MDAR'H'N TEM2		
17.41	17500VH 8.		
17.42	JPAN *+3		
17.43	MDAR STRF		

	[READS BUTTON INT9 SWITCH
	[PUTS IT INT9 SWITCH
	[TESTS FOR B9X DISPLAY
	[TESTS FOR STARS DISPLAY



20.1 ARMD'N STRF  
 20.2 MDAR'H SWITH  
 20.3 ARMD'H IBLK  
 20.4 JPAN INITL  
 20.5 17500VH 1  
 20.6 N99P  
 20.7 JPAN .+2  
 20.10 JUMP DEBEX  
 20.11 JUMP DEBEX  
 20.12 DISPT: JUMP .  
 20.13 ARMD NUMBER  
 20.14 MDAR'N'L: 4  
 20.15 ARMD BCOUNTER  
 20.16 MDAR BCOUNTER  
 20.17 JPLS .+3  
 20.20 MDAR'F NUMBER1-1  
 20.21 JUMP .+2  
 20.22 MDAR'F NUMBER2-1  
 20.23 ARMD STOLAC  
 20.24 MDAR NUMBER  
 20.25 ARMD SAVEN  
 20.26 TESTC: MDAR SAVEN  
 20.27 17500VH 3  
 20.30 ARMD SAVEN  
 20.31 ARAR'H'F  
 20.32 JPLS INSERTN  
 20.33 MDAR BLANK  
 20.34 INSERT: ARMD'IX STOLAC  
 20.35 MDAR'N'X'H BCOUNTER  
 20.36 JPAN DISEND  
 20.37 JUMP TESTC  
 20.40 INSERTN: MDAR'A MASK  
 20.41 MDAR'G SIXTY  
 20.42 ARAR'F  
 20.43 JUMP INSERT

[FOR AGT-2  
 [TESTS FOR NEW GAME

INITILIZE T9 NUMBER -1

[SHIFT 3 LEFT FOR 1 ST NUMBER

[TEST FOR END OF NUMBERS  
 [D9NE  
 [SHIFT 1 LEFT FOR NUMBER  
 [GET ONLY NUMBER  
 [SHIFT 1 LEFT FOR CORRECT POSIT





21.1	DISEND: MDAR ST9LAC	
21.2	MDAS'F'N 1	
21.3	ARM'D ST9LAC	
21.4	MDAR'I ST9LAC	
21.5	MDAR'0 DECP	
21.6	ARM'D'I ST9LAC	(ADD IN DEC PT
21.7	MDR DISPT	
21.10	ST9LAC: 0	
21.11	BCOUNTER: 0	
21.12	BLANK: 100	
21.13	SIXTY: 60	
21.14	NMASK: 7	
21.15	DECP: 134VH	
21.16	SAVEN: 0	
21.17	NUMBER: 0	
21.20	0NETW9F: 0	
21.21	DISPDATA: JUMP .	
21.22	JPSR FIRECENTL	
21.23	JUMP .	
21.24	DISPO: 0	
21.25	MDAR'N CTHF	CTEST FOR REFIGURED DISPLAY
21.26	JPAN DPEND	
21.27	ARM'D CTHF	
21.30	ARXB'F	
21.31	ARM'D 0NETW9F	
21.32	MDAR MILES	
21.33	17500VH 2	
21.34	JPSR DISPT	
21.35	MDAR'F 1	
21.36	ARM'D 0NETW9F	
21.37	MDAR TCOUNT	
21.40	17500VH 2	
21.41	ARM'D TEMPDISP	ISTORE TEMP IN TEMPDISP
21.42	MDAS'N DATATEMP	
21.43	MDAS'N TEMPDISP	



```

22.1  N88P
22.2  MDAR TEMPDISP
22.3  ARMD DATATEMP [STORE NEW IN OLD AND DISPLAY
22.4  JPSR DISPT
22.5  MDAR'N EXP1
22.6  MDAR'I'H'0 DATASTORE
22.7  N88P
22.10 DPEND: JPSR FC2
22.11 JUMP .
22.12 DISP9: N88P
22.13 MDIR DISPDATA [RETURN
22.14 DISP8: N88P
22.15 JUMP'I DISP7
22.16 DISP7: DISP9
22.17 DISP1: DISPO
22.20 DISP2: N88P
22.21 JUMP'I DISP1
22.22 MILES: 65432
22.23 TEMPDISP: 0
22.24 DATATEMP: 0
22.25 MACRO1 DATADISP (FL,ADD,DADR)
22.26 MDAR FL
22.27 JPLS .+2
22.30 JUMP ADD
22.31 MDAR'H SIMTIME
22.32 JPAN .+10
22.33 ARX0'F
22.34 ARMD FL
22.35 MDAR FLGNE
22.36 ARMD FL+1
22.37 JPSR EXP9F
22.40 ARMD'N SIMTIME
22.41 JUMP ADD
22.42 MDAR'F DADR
22.43 JPSR DISPDATA

```



23.1 ENDM  
23.2 MACR01 UPDATE (FL,ADD)  
23.3 MDAR FL  
23.4 JPLS \*+2  
23.5 JUMP ADD  
23.6 MDAR'N SIMTIME  
23.7 JPAN ADD  
23.8 ARX0'F  
23.9 ARMD FL  
23.10 MDAR FLG0NE  
23.11 ARMD FL+1  
23.12 MDAR'N DISPTIME  
23.13 ARMD SIMTIME  
23.14 UPSR CTHETA  
23.15 ARX0'F  
23.16 ARMD'N CTHF  
23.17 ENDM  
23.18 STOVER: JUMP \*  
23.19 ARAR'X SIMTIME  
23.20 ARAR'X SIMTIME2  
23.21 ARAR'X SIMTIME3  
23.22 MDAR SIMF1  
23.23 JPLS \*+2  
23.24 JUMP DISWD1  
23.25 MDAR'N SIMTIME  
23.26 JPAN DISWD1  
23.27 MDAR'N SIMTEN  
23.28 ARMD SIMTIME  
23.29 ARMD SIMTIME3  
23.30 UPSR CTHETA  
23.31 MDAR FLG0NE  
23.32 ARMD SIMF19  
23.33 MDAR'N'N MILES  
23.34 MDAR'N'N MILES  
23.35 MDAR'N'N MILES  
23.36 MDAR'N'N MILES  
23.37 MDAR'N'N MILES  
23.38 MDAR'N'N MILES  
23.39 MDAR'N'N MILES  
23.40 MDAR'N'N MILES  
23.41 MDAR'N'N MILES  
23.42 MDAR'N'N MILES  
23.43 MDAR'N'N MILES



24.1	ARX0'F
24.2	ARM0 TCGUNT
24.3	ARM0 SIMF1
24.4	MDAR FLGNE
24.5	ARM0 SIMF2
24.6	ARM0 SIMF4
24.7	ARM0 THET2F
24.10	JPSR RNDY2
24.11	77
24.12	MDAS'L: 100
24.13	ARM0'N SIMTIME
24.14	MDAR YAW1
24.15	MDAS'N THETA
24.16	ARM0'H THET2
24.17	JUMP ENDOVER
24.20	DISWD1: MDAR SIMF2
24.21	JPLS ++2
24.22	JUMP DISWD2
24.23	MDAR YANF
24.24	JPLS ++5
24.25	MDAR'H YAW1
24.26	MDAE THET2
24.27	JPAN DISWD2
24.30	JUMP ++4
24.31	MDAR'H'N YAW1
24.32	MDAE'N THET2
24.33	JPAN DISWD2
24.34	ARX0'F
24.35	ARM0 THET2F
24.36	ARM0 SIMF2
24.37	MDAR FLGNE
24.40	ARM0 SIMF3
24.41	DISWD2: MDAR SIMF3
24.42	JPLS ++2
24.43	JUMP DISWD3





25.1	MDAR'N EXP2		
25.2	JPAN DISWD3		
25.3	ARX8'F		
25.4	ARM'D SIMF3		
25.5	JPSR RESTF		
25.6	JUMP DISWD15		
25.7	DISWD3: UPDATE (SIMF4,DISWD4)		
25.10	MDAR'N EXP1		
25.11	JPAN *+2		
25.12	JPSR RESTF		
25.13	MDAR'F WD2		
25.14	JPSR DISPDATA		
25.15	DISWD4: DATADISP (SIMF5,DISWD5,WD2)		
25.16	DISWD5: UPDATE (SIMF6,DISWD6)		
25.17	MDAR'N EXP1		
25.20	JPAN *+2		
25.21	JPSR RESTF		
25.22	MDAR'F WD3		
25.23	JPSR DISPDATA		
25.24	DISWD6: DATADISP (SIMF7,DISWD7,WD3)		
25.25	DISWD7: UPDATE (SIMF8,DISWD8)		
25.26	MDAR'N EXP1		
25.27	JPAN *+2		
25.30	JPSR RESTF		
25.31	MDAR'F WD4		
25.32	JPSR DISPDATA		
25.33	DISWD3: MDAR SIMF9		
25.34	JPLS *+2		
25.35	JUMP DISWD9		
25.36	ARX8'F		
25.37	ARM'D CTHTF		
25.40	JPSR CTHETA		
25.41	MDAR YAW2		
25.42	MDAS'N YAWC2		
25.43	ARM'D YAW2		

		DISPLAY TARGET DETECTED	
			DISPLAY DECISION T9 KILL



26.1	MDAR'H YAW2	
26.2	MDAE'H'N THETA	
26.3	JPAN .+4	
26.4	MDAR'F WD4	
26.5	JPSR DISPDATA [DISPLAY TRACKING	
26.6	JUMP DISWD9	
26.7	ARX9'F	
26.10	ARMD SIMF9	
26.11	ARMD SIMTIME	
26.12	MDAR FLGENE	
26.13	ARMD SIMF10	
26.14	JUMP END9VER	
26.15	DISWD9: UPDATE (SIMF10,DISWD10)	
26.16	MDAR'N EXP1	
26.17	JPAN .+2	
26.20	JPSR RESTF	
26.21	MDAR'F WD5	
26.22	JPSR DISPDATA	
26.23	DISWD10: DATADISP (SIMF11,DISWD11,WD5) [DISPLAY LOCKEN	
26.24	DISWD11: UPDATE (SIMF12,DISWD12)	
26.25	MDAR'N EXP1	
26.26	JPAN .+3	
26.27	JPSR RESTF	
26.30	JUMP DISWD12	
26.31	MDAR FLGENE	
26.32	ARMD FCS2F	
26.33	ARMD SIMF15	
26.34	MDAR'F 27	
26.35	ARMD'N SIMTIME2	
26.36	MDAR THETA	
26.37	ARMD YAW2	
26.40	MDAR'F WD6	
26.41	JPSR DISPDATA	
26.42	DISWD12: MDAR SIMF13	
26.43	JPLS .+2	

[TIME T9 WAIT  
UNTIL MISSILES FIRED



27.1	JUMP DISWD13	
27.2	MDAR'H SIMTME2	
27.3	JPAN DISWD13	
27.4	ARX0'F	
27.5	ARMD FCS2F	
27.6	ARMD SIMF13	
27.7	JPSR CTHETA	
27.10	MDAR MILES	
27.11	17440VH 2	
27.12	ARMD'N SIMTME2	
27.13	MDAR FLG0NE	
27.14	ARMD SIMF14	
27.15	DISWD13: MDAR SIMF14	
27.16	JPLS +2	
27.17	JUMP DISWD14	
27.20	MDAR'N EXP1	
27.21	JPAN +10	
27.22	ARX0'F	
27.23	ARMD FCS2F	
27.24	ARMD T0RP2F	
27.25	ARMD SIMF3	
27.26	ARMD SIMTME	
27.27	JPSR RESTF	
27.30	JUMP DISWD15	
27.31	MDAR'H SIMTME2	
27.32	JPAN DISWD14	
27.33	ARX0'F	
27.34	ARMD SIMF14	
27.35	ARMD SIMF15	
27.36	MDAR FLG0NE	
27.37	ARMD SIMF9	
27.40	JUMP DISWD8	
27.41	DISWD14: DATADISP (SIMF15,DISREP,WD6)	
27.42	DISREP: ARX0'F	
27.43	ARMD SIMF16	



```

30.1 DISWD15: UPDATE (SIMF16,DISWD16)
30.2 ARMD FCS2F
30.3 ARMD TBRP2F
30.4 MDAR'F WD7
30.5 JPSR DISPDATA
30.6 DISWD16: DATADISP (SIMF17,DISWD17,WD7) [DISPLAY DESTROYED TARGET
30.7 DISWD17: MDAR SIMF18
30.8 JPLS ++2
30.9 JUMP DISWD18
30.10 ARX8'F
30.11 ARMD SIMF18
30.12 ARMD SIMF2
30.13 ARMD SIMF3
30.14 DISWD13: MDAR SIMF19
30.15 JPLS ++2
30.16 JUMP ENDOVER
30.17
30.18
30.19
30.20
30.21
30.22 JPSR $R0FW
30.23 -0
30.24 115
30.25 IBLK
30.26 GETIB: 10.
30.27
30.28
30.29
30.30 JPSR $R0FW
30.31 C
30.32 116
30.33 JBLK
30.34 PUTJB: 10.
30.35 JPSR $FINSH
30.36
30.37 ENDOVER: MDIR STOVER
30.38 RESTF: JUMP . [SUBPROGRAM TO RESET
30.39 ARX9'F [AND TO DISPLAY
30.40 ARMD SIMF4 [DISPLAY TARGET DESTROYED
30.41
30.42
30.43

```

[WRITE OUT IBLK

[READ IN JBLK





31.1	ARMD	SIMF6
31.2	ARMD	SIMF7
31.3	ARMD	SIMF8
31.4	ARMD	SIMF9
31.5	ARMD	SIMF10
31.6	ARMD	SIMF11
31.7	ARMD	SIMF12
31.10	ARMD	SIMF13
31.11	ARMD	SIMF14
31.12	ARMD	SIMF15
31.13	NDAR	FLGNE
31.14	ARMD	SIMF16
31.15	NDIR	RESTF
31.16	SIMF1:	0
31.17	SIMF2:	0
31.20	SIMF3:	0
31.21	SIMF4:	0
31.22	SIMF5:	0
31.23	SIMF6:	0
31.24	SIMF7:	0
31.25	SIMF8:	0
31.26	SIMF9:	0
31.27	SIMF10:	0
31.30	SIMF11:	0
31.31	SIMF12:	0
31.32	SIMF13:	0
31.33	SIMF14:	0
31.34	SIMF15:	0
31.35	SIMF16:	0
31.36	SIMF17:	0
31.37	SIMF18:	0
31.40	SIMF19:	0
31.41	FCS2E:	0
31.42	TORPEF:	0
31.43	TWET2F:	0



32.1	THEI2: 0
32.2	FLGONE: 1
32.3	SIMTEN: 10
32.4	SIMTIME: 0
32.5	SIMTIME2: 0
32.6	SIMTIME3: 0
32.7	DISPTIME: 100
32.10	FIRECENTL: JUMP .
32.11	ARMYD CHRSTRING
32.12	JPSR COFF
32.13	JPSR SUP
32.14	DISP2
32.15	DISP2
32.16	CHRSTRING: WD2
32.17	JPSR CEN
32.20	MDIR FIRECENTL
32.21	FC2: JUMP .
32.22	ARMYD FIRESAVE
32.23	MD07'IL: 0
32.24	JPSR COFF
32.25	JPSR SUP
32.26	DISP3
32.27	DISP3
32.30	NUMBER1-3
32.31	JPSR CEN
32.32	MDAR FIRESAVE
32.33	MDIR FC2
32.34	COFF: JUMP .
32.35	MDIC'AIL
32.36	-10
32.37	MD10'AIL
32.40	776VH
32.41	MDIR COFF
32.42	CEN: JUMP .
32.43	MD10'e'IL



33.1 776JH  
 33.2 MDIC'9'L  
 33.3 10  
 33.4 MDIR CON  
 33.5 SUP: JUMP  
 33.6 MDAR'I SUP  
 33.7 ARMD 77736  
 33.10 MDAR'X SUP  
 33.11 MDAR'I SUP  
 33.12 ARMD 77737  
 33.13 MDAR'X SUP  
 33.14 MDAR'I SUP  
 33.15 ARMD 77735  
 33.16 MDAR'X SUP  
 33.17 MDIR SUP  
 33.20 FIRESAVE: 0  
 33.21 MD06= 26000JH  
 33.22 AR07= 27100JH  
 33.23 MD11= 31000JH  
 33.24 WD2: 0  
 33.25 0463005670  
 33.26 1743000000  
 33.27 5220251216  
 33.30 4265020210  
 33.31 4265042606  
 33.32 5221342100  
 33.33 WD3: 0  
 33.34 0463005670  
 33.35 1743000000  
 33.36 4221241622  
 33.37 5162247634  
 33.40 2025047500  
 33.41 4562346230  
 33.42 WD4: 0  
 33.43 0463005670

ITARGET DETECTED

IDECISION TO KILL

ITRACKING



34.1  
34.2  
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34.42  
34.43

1743000000  
5224440606  
4562347216  
WD5: 0  
0463005670  
1743000000  
2023047606  
4550147634  
WD6: 0  
0463005670  
1743000000  
4322251212  
2023244646  
5162246212  
0024700000  
WD7: 0  
0463005670  
1743000000  
4221251650  
5123654612  
4210052202  
5121742650  
0  
0464005700  
1742000000  
NUMBER1: 0  
0  
0  
0  
0  
2023244630  
4264600000  
0463005710  
5222246612  
2017220000

BLACK SN

IFIRE MISSILES

DESTROYED TARGET

NEW X,Y VALUES  
UP BRIGHTNESS,ITALICS

1 MILES, TIME = .  
NEW X,Y VALUES





```

35.1 35.1 NUMBER2: 0
35.2 0
35.3 0
35.4 0
35.5 0
35.6 2024642606
35.7 0000100000 [END OF DATA AND TEST
35.10 DBEX: MDAR B0XF
35.11 JPAN SUN
35.12 DISPLAY (FS,CO,CO,HFS,B0X)
35.13 SUN: N0BP
35.14 1SHIP: MDAR,H 0NE
35.15 ARMD SHTV1
35.16 ARX0'F
35.17 ARMD XACC1
35.20 ARMD YACC1
35.21 MDAR SCL
35.22 ARMD SSCL
35.23 MDAR RLT1
35.24 JPLS *+2
35.25 JUMP *+2
35.26 ARAR,X RLT1
35.27 MDAR G0NE1
35.30 JPLS 1SH5
35.31 MDAR EXP1
35.32 JPLS 1SH4
35.33 MDAR HYP1
35.34 JPLS 1SH6
35.35 MDAR HYPB1
35.36 JPLS 1SH7
35.37 SWITCHOFF (SPR1,TURNR1) [THIS IS THE TURN ROUTINE
35.40 MDAR YAW1
35.41 MDAS'N YAW
35.42 ARMD YAW1
35.43 TURNR4: JPSR SNC0S

```



36.1	MDAR'L:PTEN: 20	
36.2	15420VH C8SN	
36.3	N88P	
36.4	ARAR'H	
36.5	ARMD XVEL1	
36.6	MDAR PTEN	
36.7	15420VH SINE	
36.10	N88P	
36.11	ARAR'H	
36.12	ARMD YVEL1	
36.13	JUMP TURNR3	
36.14	TURNR1: MDAR THET2F	
36.15	JPLS *+2	
36.16	JUMP *+4	
36.17	MDAR YTANF	
36.20	JPLS TURNR4-3	
36.21	JUMP TURNR5	
36.22	SWITCHOFF (SPL1,TURNR2)	
36.23	TURNR5: MDAR YAW1	
36.24	MDAS YAW	
36.25	ARMD YAW1	
36.26	JUMP TURNR4	
36.27	TURNR2: MDAR YAW1	
36.30	JPSR SNC8S	
36.31	TURNR3: SWITCHOFF (SPL1,1NEXT)	
36.32	1NEXT: MDAR TVC	[DISPLAYS FIRE
36.33	ARMD'N TVC1	
36.34	JPSR RANDM	
36.35	MDAR'9'L	
36.36	7777VH 60000	
36.37	MDAS'F'N 17777	
36.40	MDAR'E C11	
36.41	ARMD'H SHTV1	
36.42	1SH4: MDAR J9LK	[PLANE
36.43	ARMD XP8S1	

[TEST FOR NEG DIRRECTION



37.1	MDAR	JBLK+1	
37.2	ARMD	YPOS1	
37.3	MDAR	JBLK+2	
37.4	ARMD	YAW1	
37.5	MDAR	JBLK+10	
37.6	ARMD	TORP2F	
37.7	MDAR	HYRT1	
37.10	JPLS	•+2	
37.11	JUMP	•+2	
37.12	ARAR'X	HYRT1	
37.13	MDAR	EXP1	
37.14	JPLS	1SH3	
37.15	1SH10:	N99P	
37.16	1SH40:	N99P	
37.17	1SH42:	N99P	
37.20	1SH41:	N99P	
37.21	1SH11:	SWITCHOFF (SPH1,1SH2)	
37.22	MDAR	HYRT1	
37.23	JPLS	1SH2	
37.24	MDAR	HYPA	
37.25	ARMD'N	HYPA1	
37.26	JUMP	1SH99	
37.27	1SH2:	MDAR TORP2F	
37.30	JPLS	1SH2P	
37.31	SWITCHOFF	(SPF1,1SH20)	
37.32	1SH2P:	MDAR TPCT1	
37.33	MDAR	TPCT1	
37.34	JPLS	•+2	
37.35	JUMP	1SH20	
37.36	MDAR	RLT1	
37.37	JPLS	1SH20	
37.40	MDAR	FTA	
37.41	JPLS	•+2	
37.42	JUMP	1SH20	
37.43	ARMD	TEM1	



40.1	ARAR'X TPCT1
40.2	MDAR'I TEM1
40.3	ARM'D FTA
40.4	ARXØ'F
40.5	ARM'D'I TEM1
40.6	MDAR TEM1
40.7	ARM'D'I LPTR
40.10	ARM'D LPTR
40.11	MDAR'L: 140
40.12	ARM'D'I'X'IN TEM1
40.13	MDAR PØFS
40.14	15420VH SINE
40.15	NØØP
40.16	MDAE'H YPØS1
40.17	MDAR'A'H AMASK
40.20	ARM'D'H TEM2
40.21	MDAR PØFS
40.22	15420VH CØSN
40.23	NØØP
40.24	MDAE'H XPØS1
40.25	MDAR'A'H AMASK
40.26	MDAR'Ø TEM2
40.27	ARM'D'I'X TEM1
40.30	MDAR TVEL
40.31	15420VH SINE
40.32	NØØP
40.33	MDAE'H YVEL1
40.34	MDAR'A'H AMASK
40.35	ARM'D'H TEM2
40.36	MDAR TVEL
40.37	15420VH CØSN
40.40	NØØP
40.41	MDAE'H XVEL1
40.42	MDAR'A'H AMASK
40.43	MDAR'Ø TEM2





41.1	ARM'D'I'X TEM1	
41.2	MDAR RL1	
41.3	ARM'D'N RL11	
41.4	1SH20: MDAR'L	
41.5	SHIP1X-1	
41.6	ARM'D'L	
41.7	SHIP1XPTR: 0	
41.10	MDAR'L	
41.11	SHIP1-1	
41.12	ARM'D'L	
41.13	SHIP1PTR: 0	
41.14	SHIP1ROT: MDAR'I'X'H SHIP1XPTR	
41.15	JPLS *+2	
41.16	JUMP 1SH20A	
41.17	ARAR'H	
41.20	JFSR ROTATE	
41.21	ARM'D'I'X SHIP1PTR	
41.22	JUMP SHIP1ROT	
41.23	1SH20A: MDAR'L	
41.24	MD05 SHIP1	
41.25	1SH21: ARM'D TEM1	
41.26	MDAR'N DISDN	
41.27	JPAN *-1	
41.30	ARM'D DISDN	
41.31	MDAR TEM1	
41.32	ARM'D EGVPV	
41.33	DISPLAY (SSCL,XPOS1,YP0S1,HFS)	
41.34	JUMP 1SH99	
41.35	1SH5: MDAR'X G9NE1	
41.36	JPLS 1SH99	
41.37	ARM'D'0 INIT	
41.40	JUMP 1SH99	
41.41	1SH6: MDAR'X HYP41	
41.42	JPLS 1SH99	
41.43	MDAR HYPRB	

[TEST F9R EXPLOSION



42.1	ARM'D'N HYPB1
42.2	JPSR RANDM
42.3	MDAR'A AMASK
42.4	ARM'D YPBS1
42.5	JPSR RANDM
42.6	MDAR'A AMASK
42.7	ARM'D XPOS1
42.10	ARX0'F
42.11	ARM'D'H 1PTBF
42.12	ARM'D'H 1PTBF+1
42.13	MDAR'0'H 6NE
42.14	ARM'D'H 1PTBF+2
42.15	ARM'D'H 1PTBF+3
42.16	MDAS'F 1
42.17	ARM'D'H 1PTBF+4
42.20	JUMP 1SH30
42.21	1SH7: MDAR'X HYPB1
42.22	JPLS 1SH30
42.23	JPSR RANDM
42.24	17500/H 7
42.25	MDAR'A SEVEN
42.26	MDAE'L
42.27	MDAR RNDL1
42.30	ARM'D +1
42.31	N60P
42.32	JPAN 1SH14
42.33	ARM'D'I'0 --2
42.34	MDAR HYRT
42.35	ARM'D'N HYRT1
42.36	JUMP 1SH99
42.37	1SH30: MDAR'L
42.40	MD05 1PTBF
42.41	JUMP 1SH21
42.42	1SH14: MDAR EXP
42.43	ARM'D'N EXPI



43.1	ARX0'F	
43.2	ARMD XGRV1	
43.3	ARMD YGRV1	
43.4	JUMP 1SH99	
43.5	1SH3: MDAR'X EXP1	
43.6	JPLS ++4	
43.7	MDAR 0ENE	
43.10	ARMD'N G0NE1	
43.11	JUMP 1SH99	
43.12	GG=0	
43.13	REPEAT 7,	
43.14	JPSR RNDM2	
43.15	17777JH 17777	
43.16	MDAR'A CM11	
43.17	ARMD 1PTBF+00	
43.20	ARMD 1PTBF+1+00	
43.21	MDAR'0 0NE	
43.22	ARMD 1PTBF+2+00	
43.23	GG=00+3	
43.24	ENDR	
43.25	MDAR'H 0NE	
43.26	ARMD 1PTBF+00	
43.27	MDAR EXP1	
43.30	MDAS'F 120.	
43.31	15440VH 6	
43.32	N00P	
43.33	ARMD SSCL	
43.34	JUMP 1SH30	
43.35	SSCL2: 10000	
43.36	YAWC2: 100	
43.37	1SH99:	
43.40	2SHIP: MDAR'H 0NE	
43.41	ARMD SHTV2	
43.42	ARX0'F	
43.43	ARMD XACC2	

[SHIP



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ARM D YACC2  
MDAR RL T2  
JPLS .+2  
JUMP .+2  
ARAR'X RL T2  
MDAR GONE2  
JPLS 2SH5  
MDAR EXP2  
JPLS 2SH4  
MDAR HYP A2  
JPLS 2SH6  
MDAR HYP B2  
JPLS 2SH7  
SWITCHOFF (SPR2, .+4)  
MDAR YAK2  
MDAS'N YAWC2  
ARM D YAW2  
SWITCHOFF (SPL2, .+4)  
MDAR YAW2  
MDAS YAWC2  
ARM D YAW2  
MDAR YAW2  
JPSR SNCBS  
JUMP 2SH4  
2SH4: NOP  
MDAR JBLK+3  
ARM D XPS2  
MDAR JBLK+4  
ARM D YPS2  
MDAR HYRT2  
JPLS .+2  
JUMP .+2  
ARAR'X HYRT2  
MDAR EXP2  
JPLS 2SH3  
2SH10: NOP

CODED FOR READ FROM AGT-2





45.1	2SH40: N00P	
45.2	2SH42: N00P	
45.3	2SH41: N00P	
45.4	2SH11: SWITCHOFF (SPH2,2SH2)	[BY HYPER BUTTON
45.5	JPSR CTHETA	
45.6	2SH2: MDAR FCS2F	
45.7	JPLS 2SH2P	
45.10	SWITCHOFF (SPF2,2SH20)	
45.11	2SH2P: MDAR TPCT2	
45.12	JPLS *+2	
45.13	JUMP 2SH20	
45.14	MDAR RLT2	
45.15	JPLS 2SH20	
45.16	MDAR FTA	
45.17	JPLS *+2	
45.20	JUMP 2SH20	
45.21	ARMD TEM1	
45.22	ARAR'X TPCT2	
45.23	MDAR'I TEM1	
45.24	ARMD FTA	
45.25	ARX9'F	
45.26	ARMD'I TEM1	
45.27	MDAR TEM1	
45.30	ARMD'I LPTR	
45.31	ARMD LPTR	
45.32	MDAR ITIME	
45.33	ARMD'I'X'N TEM1	
45.34	MDAR P6FS	
45.35	17500VH 1	
45.36	15420VH SINE	
45.37	N00P	
45.40	MDAE'H YP9S2	
45.41	MDAR'A'H AYASK	
45.42	ARMD'H TEM2	
45.43	MDAR P6FS	



46.1 15420JH C9SN  
 46.2 N88P  
 46.3 MDAE'I'H XP9S2  
 46.4 MDAR'A'H AMASK  
 46.5 MDAR'I'8 TEM2  
 46.6 ARMD'I'X TEM1  
 46.7 MDAR TVEL  
 46.10 15420JH SINE  
 46.11 N88P  
 46.12 MDAE'I'H YVEL2  
 46.13 MDAR'A'H AMASK  
 46.14 ARMD'I'H TEM2  
 46.15 MDAR TVEL  
 46.16 15420JH C9SN  
 46.17 N88P  
 46.20 MDAE'I'H XVEL2  
 46.21 MDAR'A'H AMASK  
 46.22 MDAR'I'8 TEM2  
 46.23 ARMD'I'X TEM1  
 46.24 MDAR'I'14  
 46.25 ARMD'N RL2  
 46.26 2SH20: MDAR'I'F SHIP2X-1  
 46.27 ARMD'L  
 46.30 SHIP2XPTR: 0  
 46.31 MDAR'I'F SHIP2-1  
 46.32 ARMD'L  
 46.33 SHIP2PTR: 0  
 46.34 SHIP2PRT: MDAR'I'X'I'H SHIP2XPTR  
 46.35 JPLS \*+2  
 46.36 JUMP 2SH20A  
 46.37 ARAR'I'H  
 46.40 JPSR RGATE  
 46.41 ARMD'I'X SHIP2PTR  
 46.42 JUMP SHIP2PRT  
 46.43 2SH20A: MDAR'I'L



47.1	MD05 SHIP2	
47.2	2SH21: ARM2 TEM1	
47.3	MDAR'N DISDN	
47.4	JAPN *-1	
47.5	ARM2 DISDN	
47.6	MDAR TEM1	
47.7	ARM2 EGVV	
47.10	DISPLAY (SSCL2,XP8S2,Y8S2,HFS)	
47.11	JUMP 2SH99	
47.12	2SH5: MDAR'X G8NE2	
47.13	JPLS 2SH99	
47.14	ARM2'8 INIT	
47.15	JUMP 2SH99	
47.16	2SH6: JUMP 2SH99	
47.17	2SH7: JUMP 2SH99	
47.20	2SH30: MDAR'1	
47.21	MD05 2PTBF	
47.22	JUMP 2SH21	
47.23	2SH14: MDAR EXP	
47.24	ARM2'N EXP2	
47.25	ARX8'F	
47.26	ARM2 XGRV2	
47.27	ARM2 YGRV2	
47.30	JUMP 2SH99	
47.31	2SH3: MDAR'X EXP2	
47.32	JPLS *-4	
47.33	MDAR G8NE	
47.34	ARM2'N G8NE2	
47.35	JUMP 2SH99	
47.36	QQ=0	
47.37	REPEAT 7,	
47.40	JPSR RNDM2	
47.41	17777JH 17777	
47.42	MDAR'1A CM11	
47.43	ARM2 2PTBF+QQ	

[CHECK FOR FOR EXPLOSION



50.1	ARM0 2PTBF+1+00
50.2	MDAR'0 0NE
50.3	ARM0 2PTBF+2+00
50.4	00=00+3
50.5	ENDR
50.6	MDAR'H 0NE
50.7	ARM0 2PTBF+00
50.10	MDAR EXP2
50.11	MDAS'F 120.
50.12	15440VH 6
50.13	N0CP
50.14	ARM0 SSCL
50.15	JUMP 2SH30
50.16	2SH99:
50.17	CELL: ARX0'F
50.20	ARM0 AR0VF
50.21	MDAR GENE1
50.22	MDAR'0 EXP1
50.23	MDAR'0 HYP A1
50.24	ARM0 SHF1
50.25	MDAR GENE2
50.26	MDAR'0 EXP2
50.27	MDAR'0 HYP A2
50.30	ARM0 SHF2
50.31	MDAR'0 SHF1
50.32	JPLS NECGL
50.33	MDAR'H XP0S1
50.34	MDAE'H'N XP0S2
50.35	ANX0 MZ00
50.36	ARM0'H TEM1
50.37	ARM0'H CXP0S
50.40	ARAR'F'H
50.41	15420VH TEM1
50.42	N0CP
50.43	17440VH 1





51.1	ARM D TEM1	
51.2	MDAR'H YP0S1	
51.3	MDAE'H'N YP0S2	
51.4	ANX0 YZR0	
51.5	ARM D'H TEM2	
51.6	ARM D'H CYP0S	
51.7	ARAR'F'H	
51.10	15420VH TEM2	
51.11	N00P	
51.12	17440VH 3	
51.13	MDAE TEM1	
51.14	MDAE'N'H SHPCR	
51.15	JPAN •+2	
51.16	JUMP CSW	
51.17	MDAR AR0VF	
51.20	JPAN CSW	
51.21	MDAR EXP	
51.22	ARM D'N EXP1	
51.23	ARM D'N EXP2	
51.24	ARM D'0 SHF1	
51.25	ARM D'0 SHF2	
51.26	CSW: SWITCHOFF (SPH2,N0C0L)	
51.27	JUMP N0C0L	
51.30	CP0S: 0	
51.31	CYP0S: 0	
51.32	CTHETA: JUMP •	
51.33	MDAR JBLK+6	
51.34	ARM D MILES	
51.35	MDAR JBLK+7	
51.36	ARM D THETA	
51.37	TNT1: 0	
51.40	TANEND: 0	
51.41	MDAR MILES	
51.42	ARBR'F	
51.43	MDIR CTHETA	

	[COLLISION, SET T0 60	
	[START 0F CALCULATE THETA	
	[DISPLAY MILES	



52.1 THZMAX: 37777  
 52.2 DELTH: 20  
 52.3 CTHTF: 0  
 52.4 THETA: 0  
 52.5 THETAT: 0  
 52.6 XTANF: 0  
 52.7 YTANF: 0  
 52.10 TANF: 0  
 52.11 NECOL: MDAR TSHCR  
 52.12 ARMD SH1CR  
 52.13 ARMD SH2CR  
 52.14 MDAR HYPB1  
 52.15 JPLS \*+2  
 52.16 JUMP \*+3  
 52.17 MDAR TSHC2  
 52.20 ARMD SH1CR  
 52.21 MDAR HYPB2  
 52.22 JPLS \*+2  
 52.23 JUMP \*+3  
 52.24 MDAR TSHC2  
 52.25 ARMD SH2CR  
 52.26 MDAR STRF  
 52.27 JPAN NGSTARS  
 52.30 DISPLAY (FS,CO,CO,CO,STARS1)  
 52.31 MDAR'X NSTRS  
 52.32 JPLS TRYSTARS3  
 52.33 MDAR'L'N  
 52.34 1  
 52.35 ARMD NSTRS  
 52.36 DISPLAY (FS,CO,CO,CO,STARS2)  
 52.37 TRYSTARS3: MDAR'X STARS  
 52.40 JPLS NGSTARS  
 52.41 MDAR'L'N  
 52.42 2  
 52.43 ARMD STARS



53.1 DISPLAY (FS,CO,CO,CO,STARS3)  
53.2 NOSTARS: MDAR'F TLA  
53.3 ARMD PREV  
53.4 MDAR'F TORPS-1  
53.5 ARMD SPTR  
53.6 MDAR TLA  
53.7 JPLS +2  
53.10 JUMP MSIAR  
53.11 TRP1: ARMD PTR1  
53.12 ARMD PTR2  
53.13 ARMD PTR6  
53.14 MDAS'F 2  
53.15 ARMD PTR3  
53.16 MDAS'F 1  
53.17 ARMD PTR4  
53.20 MDAR'I PTR1  
53.21 ARMD TPTR  
53.22 MDAR'I PTR3  
53.23 ARMD TEM2  
53.24 MDAE'I PTR4  
53.25 ARMD'I PTR3  
53.26 MDAR'I'X PTR2  
53.27 JPAN TRP5  
53.30 ARMD'H TEM1  
53.31 MDAS TEXP  
53.32 ARMD'I PTR2  
53.33 ARAR'F'H  
53.34 MDX9'F 4  
53.35 JPLS TRP30  
53.36 MDAR'I PTR1  
53.37 ARMD'I PREV  
53.40 JPLS +3  
53.41 MDAR PREV  
53.42 ARMD LPTR  
53.43 MDAR FTA



54.1	ARM'D'I PTR1
54.2	MDAR PTR1
54.3	ARM'D FTA
54.4	JUMP TRP91
54.5	TRP30: MDX9'F 4
54.6	MDX9 TEM1
54.7	JPLS *+2
54.10	JUMP TRP90
54.11	JPSR RNDM2
54.12	777JH 777
54.13	TRP31: MDAE'I PTR3
54.14	MDAR'A CM11
54.15	ARM'D'I'X SPTR
54.16	ARM'D'I'X SPTR
54.17	MDAS'F 1
54.20	ARM'D'I'X SPTR
54.21	JUMP TRP90
54.22	TRP5: MDAS'F 1
54.23	ARM'D'I PTR2
54.24	MDAR SHF1
54.25	JPLS TRP51
54.26	ARX9'F
54.27	ARM'D ARQVF
54.30	MDAR TEM2
54.31	MDAE'H'N XP0S1
54.32	ANX9 MZR0
54.33	ARM'D TEM1
54.34	MDAR'H TEM2
54.35	MDAE'H'N YP0S1
54.36	ANX9 MZR0
54.37	MDAE TEM1
54.40	MDAE'N'H SH1CR
54.41	JPAN *+2
54.42	JUMP TRP51
54.43	MDAR ARQVF





55.1 JPN TRP51  
55.2 ARX0'F  
55.3 ARMD'I PTR2  
55.4 MDAR EXP  
55.5 ARMD'N EXP1  
55.6 ARX0'F  
55.7 ARMD XGRV1  
55.10 ARMD YGRV1  
55.11 JUMP TRP30  
55.12 TRP51: N00P  
55.13 MDAR SHF2  
55.14 JPLS TRP52  
55.15 ARX0'F  
55.16 ARMD AR0VF  
55.17 MDAR TEM2  
55.20 MDAE'H'N XP0S2  
55.21 ANX0 MZQ  
55.22 ARMD TEM1  
55.23 MDAR'H TEM2  
55.24 MDAE'H'N YP0S2  
55.25 ANX0 MZQ  
55.26 17440VH 1  
55.27 MDAE TEM1  
55.30 MDAE'H'N SH2CR  
55.31 JPN \*+2  
55.32 JUMP TRP52  
55.33 MDAR AR0VF  
55.34 JPN TRP52  
55.35 ARX0'F  
55.36 ARMD'I PTR2  
55.37 MDAR EXP  
55.40 ARMD'N EXP2  
55.41 ARX0'F  
55.42 ARMD XGRV2  
55.43 ARMD YGRV2

LOADED TO MAKE Y DIFF 2 TIMES AS LARGE



56.1 JUMP TRP90  
56.2 TRP52: N99P  
56.3 JUMP TRP40  
56.4 TRP41: ARMD PTR6  
56.5 ARMD PTR7  
56.6 MDAS'F 2  
56.7 ARMD PTR8  
56.10 MDAR'I'X'N PTR7  
56.11 JPAN TRP40  
56.12 ARX9'F  
56.13 ARMD AR9VF  
56.14 MDAR'I PTR8  
56.15 MDAE'N TEM2  
56.16 MDAR'H'I PTR8  
56.17 MDAE'H'N TEM2  
56.20 ANX9 HMZR9  
56.21 ARAR'F'H  
56.22 ANX9 HMZR9  
56.23 ARAE'F'H  
56.24 MDAE'N'H TTCR  
56.25 JPAN TRP42  
56.26 TRP40: MDAR'I PTR6  
56.27 JPLS TRP41  
56.30 ARX9'F  
56.31 JUMP TRP31  
56.32 TRP42: MDAR AR9VF  
56.33 JPAN TRP40  
56.34 ARX9'F  
56.35 ARMD'I PTR2  
56.36 MDAR'L  
56.37 30000  
56.40 ARMD'I PTR7  
56.41 TRP90: MDAR PTR1  
56.42 ARMD PREV  
56.43 TRP91: MDAR TPTR



57.1 JPLS TRP1  
57.2 MDAR'H ONE  
57.3 ARMD'I'X SPTR  
57.4 DISPLAY (FS,CO,CO,HFS,TORPS)  
57.5 MSTAR: NBBP  
57.6 MDAR STRF  
57.7 JPAN DNE  
57.10 MDAR'F STARS  
57.11 ARMD'L  
57.12 STRMVPT: 0  
57.13 STARMVE: MDAR'I'X STRMVPT  
57.14 MDAE'L  
57.15 10/H -6  
57.16 MDAR'A CM11  
57.17 ARMD'I STRMVPT  
57.20 ARMD'X'I STRMVPT  
57.21 MDAR'G ONE  
57.22 ARMD'X'I STRMVPT  
57.23 MDAR'F STARSND  
57.24 MDAE'N STRMVPT  
57.25 JPAN +2  
57.26 JUMP STARMVE  
57.27 MDAR STARS2-1  
57.30 MDAR'G EQLBIT  
57.31 ARMD STARS2-1  
57.32 MDAR STARS3-1  
57.33 MDAR'G EQLBIT  
57.34 ARMD STARS3-1  
57.35 MDAR STARSND-1  
57.36 MDAR'G EQLBIT  
57.37 ARMD STARSND-1  
57.40 DNE: MDAR'N STRTF  
57.41 JPAN DNE1  
57.42 MDAR'N ZERR  
57.43 JUMP OUTPUT



60.1	TCOUNT: 0		
60.2	DNE1: MDAR STRTF		
60.3	JPAN OUTPUT		
60.4	JUMP DNE1		
60.5	OUTPUT: APAR'X TCOUNT		
60.6	MD07'L: 0		
60.7	JPSR STOVER		
60.10	N86P		
60.11	OUT1: JPSR C8FF		
60.12	MD10'L'0		
60.13	CLK0N		
60.14	JUMP L86P		
60.15	OUT2: OUT1		
60.16	OUT3: N86P		
60.17	JUMP'I OUT2		
60.20	N86P		
60.21	N86P		
60.22	N86P		
60.23	MACR01 *M (X,Y)		
60.24	(X\00 77776) JH (Y\00 77776)		
60.25	ENDM		
60.26	MACR01 *D (X,Y)		
60.27	(X\00 77776) JH (Y\00 77776) + 1		
60.30	ENDM		
60.31	SHIP1:		
60.32	*M (300,1)		
60.33	*M (300,1)		
60.34	*D (200,20)		
60.35	*D (1,30)		
60.36	*D (-140,25)		
60.37	*D (-200,10)		
60.40	*D (-200,-10)		
60.41	*D (-140,-25)		
60.42	*D (1,-30)		
60.43	*D (200,-20)		
60.44	*D (300,1)		





61.1 M (170,22)  
 61.2 D (-40,140)  
 61.3 D (1,30)  
 61.4 M (1,-30)  
 61.5 D (-40,-140)  
 61.6 D (170,-22)  
 61.7 M (-75,27)  
 61.10 D (-140,65)  
 61.11 D (-240,73)  
 61.12 D (-170,11)  
 61.13 M (-110,1)  
 61.14 D (-240,1)  
 61.15 M (-170,-11)  
 61.16 D (-240,-73)  
 61.17 D (-140,-65)  
 61.20 D (-75,-27)  
 61.21 M (-200,1)  
 61.22 1/H  
 61.23 SHIP2:  
 61.24 M (10,10)  
 61.25 M (10,10)  
 61.26 D (-10,10)  
 61.27 M (1,10)  
 61.30 D (1,-10)  
 61.31 M (10,-10)  
 61.32 D (-10,-10)  
 61.33 M (0,100)  
 61.34 D (20,10)  
 61.35 D (24,-30)  
 61.36 D (30,-200)  
 61.37 D (24,-330)  
 61.40 D (14,-340)  
 61.41 D (-14,-340)  
 61.42 D (-24,-330)  
 61.43 D (-30,-200)



62.1 D (-24,-30)  
 62.2 D (-20,10)  
 62.3 D (0,100)  
 62.4 M (-14,-50)  
 62.5 D (-14,-34)  
 62.6 D (-10,-24)  
 62.7 D (10,-24)  
 62.10 D (14,-34)  
 62.11 D (14,-50)  
 62.12 M (-20,-50)  
 62.13 D (20,-50)  
 62.14 D (20,-210)  
 62.15 D (-20,-210)  
 62.16 D (-20,-50)  
 62.17 M (10,-210)  
 62.20 D (10,-240)  
 62.21 D (-10,-240)  
 62.22 D (-10,-210)  
 62.23 1VH 0  
 62.24 SHIPX:  
 62.25 M (300,1)  
 62.26 M (300,1)  
 62.27 D (200,20)  
 62.30 D (1,30)  
 62.31 D (-140,25)  
 62.32 D (-200,10)  
 62.33 D (-200,-10)  
 62.34 D (-140,-25)  
 62.35 D (1,-30)  
 62.36 D (200,-20)  
 62.37 D (300,1)  
 62.40 M (170,22)  
 62.41 D (-40,140)  
 62.42 D (1,30)  
 62.43 M (1,-30)



63.1 D (-40,-140)  
 63.2 D (170,-22)  
 63.3 M (-75,27)  
 63.4 D (-140,65)  
 63.5 D (-240,73)  
 63.6 D (-170,11)  
 63.7 M (-110,1)  
 63.10 D (-240,1)  
 63.11 M (-170,-11)  
 63.12 D (-240,-73)  
 63.13 D (-140,-65)  
 63.14 D (-75,-27)  
 63.15 M (-200,1)  
 63.16 1/4 0  
 63.17 SHIV1=-2  
 63.20 SHIP2X:  
 63.21 M (10,10)  
 63.22 M (10,10)  
 63.23 D (-10,10)  
 63.24 M (1,10)  
 63.25 D (1,-10)  
 63.26 M (10,-10)  
 63.27 D (-10,-10)  
 63.30 M (0,100)  
 63.31 D (20,10)  
 63.32 D (24,-30)  
 63.33 D (30,200)  
 63.34 D (24,-330)  
 63.35 D (14,-340)  
 63.36 D (-14,-340)  
 63.37 D (-24,-330)  
 63.40 D (-30,-200)  
 63.41 D (-24,-30)  
 63.42 D (-20,10)  
 63.43 D (0,100)



```

64.1  .M (-14,-50)
64.2  .D (-14,-34)
64.3  .D (-10,-24)
64.4  .D (10,-24)
64.5  .D (14,-34)
64.6  .D (14,-50)
64.7  .M (-20,-50)
64.10 .D (20,-50)
64.11 .D (20,-210)
64.12 1/H 0
64.13 0
64.14 SHV2=-2
64.15 STARS=-2
64.16 STARS1: 0
64.17 STARS2: L8C .+150.
64.20 STARS3: L8C .+150.
64.21 STARSEND: L8C .+75.
64.22 L8C .+4
64.23 SO=77777;S77777=77777
64.24 S1=1444;S2=3106;S3=4544;S4=6174
64.25 S5=7615;S6=11224;S7=12620;S10=14176
64.26 S11=15535;S12=17053;S13=20347;S14=21616
64.27 S15=23040;S16=24232;S17=25373;S20=26501
64.30 S21=27554;S22=30571;S23=31550;S24=32467
64.31 S25=33345;S26=34161;S27=34733;S30=35441
64.32 S31=36102;S32=36477;S33=37025;S34=37305
64.33 S35=37517;S36=37661;S37=37754;S40=37777
64.34 AM=77777
64.35 MACR01 QUADRANT (VAL1,VAL2)
64.36 L8C(.+40);ENDM
64.37 MACR02 QUADRANT (VAL1,VAL2)
64.40 CC=0
64.41 REPEAT 40
64.42 DD=40-CC
64.43 VAL1 AM/H (VAL2 AM)

```





65.1	CC=CC+1	
65.2	ENDR	
65.3	ENDM	[REM
65.4	SNCOS: JUMP .	
65.5	MDAR'A AMSK	
65.6	ARM'D'H DEL	
65.7	17440VH 8.	
65.10	MDAS'FIN 77	
65.11	JPAN .+3	
65.12	ARAR'N'F	
65.13	MDAS'F 1	
65.14	MDAE'L; MDAR TBL+77	
65.15	ARIR'F	
65.16	ARM'D PT	
65.17	MDAR DEL	
65.20	JPAN SN1	
65.21	MDAR'A'F'H 377	
65.22	ARM'D FLAG	
65.23	SNO:	
65.24	16440VH 12137	
65.25	N89P	
65.26	ARM'D DEL	
65.27	15420VH DEL	
65.30	N89P	
65.31	ARAR'N'F	
65.32	MDAS'F 37777	
65.33	ARM'D F1	
65.34	15420VH PT	
65.35	AMSK: N89P 77777	
65.36	ARM'D COS	
65.37	MDAR'N DEL	
65.40	15400VH PT	
65.41	N89P	
65.42	MDAE COS	
65.43	MDAR'A'H AMSK	



66.1 ARMD'H C0S  
66.2 MDAR F1  
66.3 15400VH PT  
66.4 NG0P  
66.5 ARMD SIN  
66.6 MDAR DEL  
66.7 15420VH PT  
66.10 NG0P  
66.11 MDAE SIN  
66.12 MDX0'H FLAG  
66.13 MDAR'A'H AMSK  
66.14 ARMD'H SIN  
66.15 MDIR SNC0S  
66.16 SN1: MDX0'F'H 377  
66.17 MDAR'A'F'H 377  
66.20 ARMD'0 FLAG  
66.21 JUMP SNO  
66.22 TBL:  
66.23 QUADRANT(SN CC,S\ DD)  
66.24 QUADRANT(SN DD,-S\ CC)  
66.25 SINE:  
66.26 SIN: 0  
66.27 CGSN:  
66.30 CES: 0  
66.31 F1: 0  
66.32 PT: 0  
66.33 DEL: 0  
66.34 FLAG: 0  
66.35 EXP0F: JUMP .  
66.36 ARX0'F  
66.37 ARMD RCGUNT1  
66.40 EXP0F1: ARX0'F  
66.41 ARMD RCGUNT2  
66.42 ARMD RSUM  
66.43 ARAR'X RCGUNT1

EXP0ENTIAL RANDOM NUMBER GENERATOR

ADDITIVE TYPE



67.1 JPSR RANDM  
 67.2 MDAR'A RAMSK  
 67.3 ARMD RTEST  
 67.4 JPSR RANDM  
 67.5 MDAR'A RAMSK  
 67.6 ARAR'X RCBUNT2  
 67.7 MDAS RSUM  
 67.10 ARMD RSUM  
 67.11 MDAR'H RSUM  
 67.12 MDAE'N RTEST  
 67.13 JPAN \*+4  
 67.14 JPSR RANDM  
 67.15 MDAR'A RAMSK  
 67.16 JUMP \*-10  
 67.17 MDAR RCBUNT2  
 67.20 MDAR'A'L; 1  
 67.21 JPLS \*+2  
 67.22 JUMP EXPBF1  
 67.23 MDAR RCBUNT1  
 67.24 MDAS'N'L; 1  
 67.25 ARAR'F'K  
 67.26 ARMD RCBUNT1  
 67.27 MDAR RTEST  
 67.30 1744OVH 10  
 67.31 N80P  
 67.32 MDAS RCBUNT1  
 67.33 MDIR EXPBF  
 67.34 RCBUNT1: 0  
 67.35 RCBUNT2: 0  
 67.36 RTEST: 0  
 67.37 RSUM: 0  
 67.40 RAMSK: 3777  
 67.41 DATASTORE: 0

CTEST FOR 9DD



70.1  
70.2  
70.3  
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IBLK: 0  
0  
0  
0  
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0  
0  
0  
0  
0  
0  
0  
0  
JBLK: 3777  
7777  
4000  
5777  
5777  
2000  
2000  
3000  
0  
0  
TERMINATE

[XP0S1

[YPS1  
[YAW1  
[XPS2  
[YPS2  
[YAW2  
[RANGE  
[BEARING  
[TGRPF

DATA FR9M AGT-2





1.1  
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1.30

# COMPUTER PROGRAM B

## RADAR AND SHIP INFORMATION SIMULATOR

(RADAR)

```

*****
[ THIS PROGRAM SIMULATES A REAL TIME SYSTEM,
[ A DLGN-36, USS CALIFORNIA, AND A F-111 FIGHTER.
[ THE SIMULATION MODELS A HIGH SPEED ATTACK ON THE
[ SHIP, THE PLANE DETECTS THE SHIP AT 40 MILES
[ AND RELEASES AN ANTI SHIP MISSILE AT 20 MILES.
[ THE SHIP DETECTS THE PLANE AT 100 MILES AND
[ RELEASES 2 ADVANCED TERRIER MISSILES AS S99N
[ AS POSSIBLE WITH THE DETERMINED DELAYS.
[ ALL SYSTEMS ARE SIMULATED WITH THE DELAYS
[ BEING CALCULATED BY THE COMPUTER.
[
[ H.D.K. 8/12/69
[ D.A.T. UBC 10/20/69
[ E. A. POTTER 1/9/70 M9D 70-11-9
*****

```



```

2.1  EXPUNGE
2.2  TITLE RADAR
2.3  ENTRY IN11, L00P, THRST, BOX, RANDM, CLOCK, ROTATE, IN1TL, STARSSEN
2.4  ENTRY IN11, L00P, 1SHIP, 1SH4, 1SH41, 1SH2, 1SH6, 2SHIP
2.5  ENTRY 2SH4, 2SH41, 2SH2, 2SH6, CELL, TRYSTARS3, TRP1, TRP5, *STAR
2.6  ENTRY STARM9VE, SHIP1, SHIP1X, FIRECENTL, STOVER, DISPT, OUTPUT, DISPDATA
2.7  ENTRY EXP0F, DATASTORE, CTHETA, THETA
2.8  ENTRY GETIB, PUTJB
2.9  MACR01 SCL: SCLXX ENDM
2.10 MACR01 L00P: L00PX ENDM
2.11 MACR01 SWITCH0FF (NN, L0C)
2.12 MACR01 SWITCH
2.13 MDAR'N SWITH
2.14 17500VH 13. + NN
2.15 N00P
2.16 JPN L0C
2.17 ENDM
2.18 MACR01 DISPLAY (SCALE, DX, DY, DZ, L0C)
2.19 IFSAME (L0C)()
2.20 ELSE
2.21 MDAR'N DISDN
2.22 JPN *-1
2.23 ARMD DISDN
2.24 MDAR'L
2.25 MD05 L0C
2.26 ARMD E0VPV
2.27 ENDC
2.28 MDAR SCALE
2.29 15420VH PS
2.30 N00P
2.31 MDAR'L'A
2.32 7777VH 0
2.33 71100VH
2.34 26000VH DZ
2.35 MDAR DX
2.36 15420VH PS
2.37
2.38
2.39
2.40
2.41
2.42
2.43

```

[T9 TEST SWITCH ACTIONS  
 [ARLS 13. + N  
 [DISPLAY DONE  
 [STARTS AVG WITH LIST (L0C)  
 [MULT LOWER  
 [AR11'F  
 [MD06 (SET INTENSITY)  
 [MULT LOWER



```

3.1 N86P
3.2 MDAR'L'A
3.3 77777JH
3.4 ARMD DAT
3.5 MDAR DY
3.6 15420VH PS
3.7 N86P
3.8 17440VH 15. [ARRS 15.
3.9 N86P
3.10 MDAR'L'A
3.11 77777
3.12 MDAR'B DAT
3.13 67100VH
3.14 MD10'A AVGEF [STEPS CLCK
3.15 MD10'G AVGEN [STARTS AVG
3.16 MD10'G AVGPV
3.17 [MDDS L9C
3.18 ENDM
3.19 MD05=25000VH; MD07=27000VH; MD10=30000VH
3.20 SPL1=4; SPR1=5; SPH1=9.; SPT1=13.; SPI1=0
3.21 SPL2=16.; SPR2=12.; SPH2=8.; SPT2=-1
3.22 [CLKN = CLCK SETTING; ARBPV = OVERFLOW PIVOT
3.23 CLKN=1000VH; FRMPV=77755; EBLPV=77757; ARBPV=77771
3.24 THRST: 14
3.25 TVC: 4
3.26 TVC: 4
3.27 TVC: 4
3.28 TVC: 4
3.29 TVC: 4
3.30 TVC: 4
3.31 TVC: 4
3.32 TVC: 4
3.33 TVC: 4
3.34 TVC: 4
3.35 TVC: 4
3.36 TVC: 4
3.37 TVC: 4
3.38 TVC: 4
3.39 TVC: 4
3.40 TVC: 4
3.41 TVC: 4
3.42 TVC: 4
3.43 TVC: 4

```



```

TSHC2: 500
TTCR: 500
SCL: 2000
PS: 23000
YAW: 40
NSTRS: -1
STRXM: 10
SUNBM: 0
RLT: 200
FUEL: 2400.
TEXP: 6000
KGRV: 100
HYRT: 120.
DAT: 0
IREPEAT Q,(1,2)
RLTAQ: 0
GONEAQ: 0
EXPAQ: 0
HYPAQ: 0
HYBPAQ: 0
XACCAQ: 0
YACCAQ: 0
FUELAQ: -0
TVCAQ: -0
XVELAQ: 0
YVELAQ: 0
XGRAQ: 0
YGRAQ: 0
HYRTAQ: 0
TPCTAQ: 0
SHAQ: 0
SHAQ/CR: 0
QIPTBF: 0
RNDLAQ: REPEAT 8., 0; ENDR [8 RANDOM NUMBERS EACH
ENDI

```





```

5.1  BX: 07000JH 54000
5.2  07000JH 54000
5.3  37776JH 54001
5.4  37776JH 40001
5.5  07000JH 40001
5.6  07000JH 54001
5.7  07001JH 54001
5.10 TERPS: 0
5.11 TERPA: 0
5.12 YAW1: 20000
5.13 YAW2: 60000
5.14 XPS1: 60000
5.15 YPS1: 60000
5.16 XPS2: 57777
5.17 YPS2: 57777
5.20 INIT: 0
5.21 DONEF: 0
5.22 STRTF: 0
5.23 SUNB: -0
5.24 DISDN: -0
5.25 STPTR: STARS-1
5.26 FTA: 0
5.27 TLA: 0
5.30 TEM1: 0
5.31 TEM2: 0
5.32 TPTR: 0
5.33 ARQVF: 0
5.34 SSCL: 0
5.35 PTR1: 0
5.36 PTR2: 0
5.37 PTR3: 0
5.40 PTR4: 0
5.41 PTR6: 0
5.42 PTR7: 0
5.43 PTR8: 0

```

# [STARTING DATA FOR FIGURES

```

[STAR P9INTER
[INIT TERPA +134
[INIT TERPA +4
[AR OVERFLOW STORAGE
[TERPED9 POINTERS

```



6.1	LPR: 0		
6.2	SPTR: 0	INIT T9RPS -1	
6.3	PREV: 0		
6.4	SWITH: 0	ISWITCH H	
6.5	SWITL: 0	ISWITCH L	
6.6	NSIR: 0	IN9 STARS	
6.7	SDFLG: 0		
6.10	SRNT: 0		
6.11	STTEM: 0		
6.12	B8XF: 0	IB9X FLAG	
6.13	GRVF: 0	IGRAVITY FLAG	
6.14	STRF: 0		
6.15	KILSN: 0		
6.16	ZERA:CO: 0		
6.17	AMASK: 77777		
6.20	ONE:CO1: 1		
6.21	E8LSIT: 1VH 0		
6.22	CM11: -(1VH 1)		
6.23	SEVEN: 7		
6.24	C11: 1VH 1		
6.25	MZRB: -0		
6.26	HMZRB: 77777VH		
6.27	GHAGF: -20VH		
6.30	FS: 37777	DISPLAY RANDOM NUMBER GENERATOR	
6.31	HFS: 20000		
6.32	AVG8F: CLK8N 101000		
6.33	AVGEN: 60740VH		
6.34	RANDM: JUMP •	ISIMPLE RANDOM NUMBER GENERATOR	
6.35	MDARTH RNDN		
6.36	MDAE:FB 53110		
6.37	MDX9 *-1		
6.40	ARMR RNDN		
6.41	MDIR RANDM		
6.42	RNDN: 0		
6.43	RNDM2: JUMP •	ITHREE STEP RANDOM N9 GEN	



7.1	JPSR RANDM	
7.2	17500VH 5	
7.3	MDAR'A'I RNDM2	
7.4	ARM'D RNDT	
7.5	JPSR RANDM	
7.6	JPSR RANDM	
7.7	17500VH 7	
7.10	MDAR'A'I RNDM2	
7.11	MDAE RNDT	
7.12	MDAE'I'N RNDM2	
7.13	MDIR'X RNDM2	
7.14	RNDT: 0	
7.15	ARSS: JUMP • [FGR OVERFLOW	
7.16	ARM'D'9 ABOVE	
7.17	MDIR ARSS	
7.20	NULL: JUMP • [FGR INDIRRECT JUMP	
7.21	JUMP'I NULL	
7.22	CLOCK: JUMP • [UPDATES CLOCK	
7.23	ARM'D EGLAR	
7.24	MDAR'H'X D9NEF	[TEST D9NE FLAG
7.25	JPAN *+2	
7.26	ARM'D'9 STRTF	[IF YES, START FLAG
7.27	MDAR EGLAR	
7.30	JUMP'I CLOCK	
7.31	EGLAR: 0	
7.32	EGL: JUMP •	[END 9F LIST, SET DISPLAY D9NE
7.33	ARM'D'9 DISDN	
7.34	JUMP'I EGL	
7.35	CGRV: JUMP • [CALCULATES GRAVITY	
7.36	MDIR CGRAV	
7.37	R0TATE: JUMP •	[CALCULATES ROTATION
7.40	MDIR ROTATE	
7.41	TEMROTATE: 0	
7.42	XROTATE: 0	
7.43	YROTATE: 0	



10.1	XYRSTATE: 0		
10.2	RADAR: JUMP •	CENTRY POINT	
10.3	MDAR'I RADAR		
10.4	JPLS •+2		
10.5	JUMP INIL-4		
10.6	ARM THRT		
10.7	MDAR'I'X RADAR		
10.10	ARMD TVEL		
10.11	MDAR'X'I RADAR		
10.12	ARMD KGRAV		
10.13	MDAR'L: 57777	[SET INITIAL POSITION 9F SHIP	
10.14	ARMD XP9S2		
10.15	ARMD YP8S2		
10.16	INITL: MDAR'F NULL		
10.17	ARMD FMPV		
10.20	MD10 ZERO		
10.21	MDAR'F EGL		
10.22	ARMD EGLPV		
10.23	MDAR'L		
10.24	UPSR AR9SB		
10.25	ARMD AR9PV		
10.26	MDAR'F STARS		
10.27	ARMD'L		
10.30	STARSPTR: 0		
10.31	STARSGEN: N99P		
10.32	ARXG'F		
10.33	ARMD RLTI		
10.34	ARMD G9NE1		
10.35	ARMD EXP1		
10.36	ARMD HYPAl		
10.37	ARMD HYPB1		
10.40	ARMD XVEL1		
10.41	ARMD YVEL1		
10.42	ARMD XGRV1		
10.43	ARMD YGRV1		
		[SET CORE TO ZERO	
		[START 9F INITIALIZATION	
		[START CLOCK	





11.1	ARM D HRT1	
11.2	ARM D RNDL1	
11.3	ARM D RNDL1+1	
11.4	ARM D RNDL1+2	
11.5	ARM D RNDL1+3	
11.6	ARM D RNDL1+4	
11.7	ARM D RNDL1+5	
11.10	ARM D RNDL1+6	
11.11	ARM D RNDL1+7	
11.12	ARM D '6 TVC1	
11.13	ARM D RL12	
11.14	ARM D GNE2	
11.15	ARM D EXP2	
11.16	ARM D HPA2	
11.17	ARM D HPS2	
11.20	ARM D XVEL2	
11.21	ARM D YVEL2	
11.22	ARM D XGRV2	
11.23	ARM D YGRV2	
11.24	ARM D HRT2	
11.25	ARM D RNDL2	
11.26	ARM D RNDL2+1	
11.27	ARM D RNDL2+2	
11.30	ARM D RNDL2+3	
11.31	ARM D RNDL2+4	
11.32	ARM D RNDL2+5	
11.33	ARM D RNDL2+6	
11.34	ARM D RNDL2+7	
11.35	ARM D TCOUNT	
11.36	ARM D FCS2F	
11.37	ARM D IBLK+10	CT9RP2F
11.40	ARM D SIMF2	
11.41	ARM D SIMF3	
11.42	ARM D SIMF4	
11.43	ARM D SIMF5	



12.1	ARMD SIMF6
12.2	ARMD SIMF7
12.3	ARMD SIMF8
12.4	ARMD SIMF9
12.5	ARMD SIMF10
12.6	ARMD SIMF11
12.7	ARMD SIMF12
12.10	ARMD SIMF13
12.11	ARMD SIMF14
12.12	ARMD SIMF15
12.13	ARMD SIMF16
12.14	ARMD SIMF17
12.15	ARMD SIMF18
12.16	ARMD DATATEMP
12.17	ARMD'9 TVC2
12.20	ARMD JBLK
12.21	ARMD TLA
12.22	ARMD TORPA
12.23	ARMD STRF
12.24	ARMD'9 DISDN
12.25	ARMD'9 SUNB
12.26	MDAR'L
12.27	20000
12.30	ARMD YAW2
12.31	MDAR'L; 4000
12.32	ARMD YAW1
12.33	MDAR'L; 3777
12.34	ARMD XP8S1
12.35	MDAR'L; 7777
12.36	ARMD YP8S1
12.37	MDAR'L; -20
12.40	ARMD XVEL1
12.41	MDAR'L; 1
12.42	ARMD YVEL2
12.43	ARMD SIMF1

CVICE INIT

LADED



13.1	MDAR'L: 3000
13.2	ARM0 THETA
13.3	MDAR SIMTEN
13.4	ARM0'N SIMTIME
13.5	MDAR'N TPCT
13.6	ARM0 TPCT1
13.7	ARM0 TPCT2
13.10	MDAR'N FUEL
13.11	ARM0 FUEL1
13.12	ARM0 FUEL2
13.13	MDAR'L
13.14	TERPA+4
13.15	ARM0 TEM1
13.16	JPSR ST0VER
13.17	INI1: N88P
13.20	MDAR'F CL0CK
13.21	ARM0 FRMPV
13.22	
13.23	MDAR'H'X NAMEC
13.24	JPAN STN4
13.25	MDAR'L: -400
13.26	ARM0 DONEF
13.27	MDAR -2
13.30	ARM0 NAMEC
13.31	STN2: JPSR C9FF
13.32	JPSR SUP
13.33	STNAM
13.34	STNAM
13.35	NAME
13.36	MDAR'H'X NAMEC
13.37	JPAN STN2
13.40	JPSR C6N
13.41	JUMP *
13.42	STNAM: N88P
13.43	JUMP'1 STN1

# IF0R SETUP









15.1	40VH	
15.2	SSAR'F'H	CREADS BUTTON ACTIONS INTO AR
15.3	17500VH 1	
15.4	MDAR'IL'A	
15.5	1VH 77776	
15.6	ARMN SWITH	CPJTS IT INTO SWITH
15.7	MDIC'A'IL	
15.10	-40VH	
15.11	SSND TEM1	
15.12	MDAR'IL TEM1	
15.13	MDAR'IA ONE	
15.14	MDAR'IO SWITH	
15.15	ARMN SWITH	
15.16	ARMN TEM1	
15.17	MDAR'IA'H'IL	
15.20	6	
15.21	MDAR'IO SWITH	
15.22	ARMN SWITH	
15.23	MDAR SWITH	
15.24	MDX8'A SWITH	
15.25	ARMN TEM2	
15.26	MDAR'IL SWITH	
15.27	JPAN INITL	
15.30	17500VH 1	CTESTS FOR NEW GAME
15.31	N86P	
15.32	JUMP D880X	
15.33	DISPT: JUMP .	
15.34	ARMN NUMBER	
15.35	MDAR'IN'IL: 4	
15.36	ARMN SCOUNTER	DISPLAYS TCOUNT
15.37	MDAR NETWOF	
15.40	JPLS .+3	
15.41	MDAR'IF NUMBER1-1	
15.42	JUMP .+2	
15.43	MDAR'IF NUMBER2-1	



16.1	ARM0 ST0LAC	INITIALIZE T0 NUMBER -1
16.2	MDAR NUMBER	
16.3	ARM0 SAVEN	
16.4	TESTC: MDAR SAVEN	
16.5	17500/H 3	LSHIFT 3 LEFT FOR 1 ST NUMBER
16.6	ARM0 SAVEN	
16.7	ARAR'H'F	
16.10	JPLS INSERTN IALSO TESTS FOR LEADING BLANKS	
16.11	MDAR BLANK	
16.12	INSERT: ARMD'I'X ST0LAC	
16.13	MDAR'N'X'H B0COUNTER	TESTS FOR END 0F NUMBERS
16.14	JPAN DISEND	CD9NE
16.15	JUMP TESTC	LSHIFT 1 LEFT FOR NUMBER
16.16	INSERTN: MDAR'A N'MASK	LG0T 9NLY NUMBER
16.17	MDAR'0 SIXTY CADD IN 60	
16.20	ARAR'B'F	LSHIFT 1 LEFT FOR CORRECT P9SIT
16.21	JUMP INSERT	
16.22	DISEND: MDAR ST0LAC	
16.23	MDAS'F'N 1	
16.24	ARM0 ST0LAC	
16.25	MDAR'I ST0LAC	
16.26	MDAR'0 DE0PT	
16.27	ARM0'I ST0LAC CADD IN A DEC PT TO MAKE MILES	
16.30	MDIR DISPT	
16.31	ST0LAC: 0	
16.32	B0COUNTER: 0	
16.33	BLANK: 100	
16.34	SIXTY: 60	
16.35	N'MASK: 7	
16.36	DE0PT: 134/H	
16.37	SAVEN: 0	
16.40	NUMBER: 0	
16.41	ENETWAF: 0	
16.42	DIS0DATA: JUMP .	
16.43	JPSR FIRE0NTL	



17.1	JUMP •		
17.2	DISPO: 0		
17.3	ARX9'F		
17.4	ARMD 0NETW0F		
17.5	MDAR MILES		
17.6	17500VH 2		
17.7	JPSR DISPT		
17.10	MDAR'F 1		
17.11	ARMD 0NETW0F		
17.12	MDAR THETA		
17.13	17500VH 2		
17.14	JPSR DISPT		
17.15	DPRND: JPSR FC2		
17.16	JUMP •		
17.17	DISP9: N00P		
17.20	MDIR DISPDATA (RETURN		
17.21	DISP8: N00P		
17.22	JUMP'1 DISP7		
17.23	DISP7: DISP9		
17.24	DISP1: DISPO		
17.25	DISP2: N00P		
17.26	JUMP'1 DISP1		
17.27	MILES: 65432		
17.30	TEMPDISP: 0		
17.31	DATATEMP: 0		
17.32	STOVER: JUMP •		
17.33	ARAR'X SIMTIME		
17.34	ARAR'X SIMTIME2		
17.35	MDAR SIMF1		
17.36	JPLS •+2		
17.37	JUMP DISWD1		
17.40	MDAR'1H SIMTIME		
17.41	JPAN DISWD1		
17.42	MDAR'1N SIMTEN		
17.43	ARMD SIMTIME		

[CHANGED FROM TC9UNT

[DELAY 9F 10 TIME UNITS



20.1  
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20.44

ARMYD SIMTIME2  
JPSR CTHETA  
MDAR'N'H MILES  
MDAE'L: 2700VH  
JPAN DISWD1  
ARXO'F  
ARMYD SIMF1  
MDAR FLG9NE  
ARMYD SIMF2  
ARMYD SIMF3  
ARMYD SIMF4  
ARMYD THET2F  
MDAR YAW1  
MDAS'N THETA  
ARMYD'H THET2  
DISWD1: N99P  
MDAR'F IBLK  
ARMYD IBLKF  
MDAR XP8S1  
ARMYD'I IBLKF  
MDAR YP8S1  
ARMYD'I'X IBLKF  
MDAR YAW1  
ARMYD'I'X IBLKF  
MDAR XP8S2  
ARMYD'I'X IBLKF  
MDAR YP8S2  
ARMYD'I'X IBLKF  
MDAR YAW2  
ARMYD'I'X IBLKF  
MDAR MILES  
ARMYD'I'X IBLKF  
MDAR THETA  
ARMYD'I'X IBLKF

1100 MILES

CTRANSFERS DATA TO IBLK

DISWD2: N99P CWRITES AND READS DATA FROM AGT-1





21.1	MDAR'F WD4	
21.2	JPSR DISDATA (TEST	
21.3	MDAR'H SIMTIME	
21.4	JPAN .+4	
21.5	MDAR'N DISPTIME	
21.6	ARMD SIMTIME	
21.7	JPSR CTHETA	
21.10		
21.11	JPSR \$R0FW	
21.12	-0	
21.13	115	
21.14	IBLK	
21.15	GETIB: 10.	
21.16		
21.17	JPSR \$R0FW	
21.20	0	
21.21	116	
21.22	JBLK	
21.23	PUTJB: 10.	
21.24	JPSR \$FINSH	
21.25	DISWD3: MDAR SIMF4	
21.26	JPLS .+2	
21.27	JUMP DISWD4	
21.30	MDAR YANF	
21.31	JPLS .+5	
21.32	MDAR'H YAW1	
21.33	MDAE THET2	
21.34	JPAN DISWD4	
21.35	JUMP .+4	
21.36	MDAR'H'N YAW1	
21.37	MDAE'N THET2	
21.40	JPAN DISWD4	
21.41	ARX0'F	
21.42	ARMD THET2F	
21.43	ARMD SIMF4	

[100

[WRITE BUT IBLK

[READ IN JBLK



22.1	MDAR FLGONE	
22.2	ARMD SIMF5	
22.3	MDAR YAW1	
22.4	ARMD'H THET2	
22.5	DISWD4: MDAR SIMF5	
22.6	JPLS *+2	
22.7	JUMP DISWD5	
22.10	ARX8'F	IF NOT WITHIN RANGE
22.11	ARMD IBLK+10	
22.12	MDAR'N'H MILES	
22.13	MDAE'IL: 1000VH	
22.14	JPAN *+10	
22.15	MDAR'H YAW1	
22.16	MDAE'N THET2	IGN COURSE
22.17	ANXG MZR0	
22.20	ARAR'H'F	
22.21	JPLS *+3	
22.22	MDAR FLGONE	
22.23	ARMD IBLK+10	IFIRE
22.24	JUMP END0VER	
22.25	DISWD5: N00P	
22.26	DISWD6: N00P	
22.27	DISWD7: N00P	
22.30	DISWD8: N00P	
22.31	DISWD9: N00P	
22.32	DISWD10: N00P	
22.33	DISWD11: N00P	
22.34	DISWD12: N00P	
22.35	DISWD13: N00P	
22.36	DISWD14: N00P	
22.37	DISWD15: N00P	
22.40	DISWD16: N00P	
22.41	DISWD17: N00P	
22.42	END0VER: MDIR	ST0VER
22.43	SIMF1: 0	
22.44	SIMF2: 0	



23.1	SIMF3: 0
23.2	SIMF4: 0
23.3	SIMF5: 0
23.4	SIMF6: 0
23.5	SIMF7: 0
23.6	SIMF8: 0
23.7	SIMF9: 0
23.10	SIMF10: 0
23.11	SIMF11: 0
23.12	SIMF12: 0
23.13	SIMF13: 0
23.14	SIMF14: 0
23.15	SIMF15: 0
23.16	SIMF16: 0
23.17	SIMF17: 0
23.20	SIMF18: 0
23.21	FCSEF: 0
23.22	THETRF: 0
23.23	THET2: 0
23.24	FLG9NE: 1
23.25	SIMTEN: 10.
23.26	SIMTIME: 0
23.27	SIMTIME2: 0
23.30	DISPIME: 100.
23.31	FIRECENTL: JUMP
23.32	ARMCD CHRSTRING
23.33	JPSR COFF
23.34	JPSR SUP
23.35	DISP2
23.36	DISP2
23.37	CHRSTRING: NOPP
23.40	JPSR CBN
23.41	MDIR FIRECENTL
23.42	FC2: JUMP
23.43	ARMCD FIRESAVE



24.1	MD07'L: 0
24.2	JPSR C6FF
24.3	JPSR SUP
24.4	DISP8
24.5	DISP8
24.6	NUMBER1-3
24.7	JPSR C6N
24.10	MDAR FIRESAVE
24.11	MDIR FC2
24.12	C6FF: JUMP .
24.13	MDIC'A'L
24.14	-10
24.15	MD10'A'L
24.16	776VH
24.17	MDIR C6FF
24.20	C6N: JUMP .
24.21	MD10'8'L
24.22	776VH
24.23	MDIC'8'L
24.24	10
24.25	MDIR C6N
24.26	SUP: JUMP .
24.27	MDAR'I SUP
24.30	ARM0 77736
24.31	MDAR'X SUP
24.32	MDAR'I SUP
24.33	ARM0 77737
24.34	MDAR'X SUP
24.35	MDAR'I SUP
24.36	ARM0 77735
24.37	MDAR'X SUP
24.40	MDIR SUP
24.41	FIRESAVE: 0
24.42	MD06= 26000VH
24.43	AR07= 27100VH
24.44	MD11= 31000VH





25.1  
25.2  
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WD4: 0  
0463005670  
1743000000  
5224440606  
4562347216  
0  
0464005700  
1742000000  
NUMBER1: 0  
0  
0  
0  
0  
0  
2023244630  
4264600000  
0463005710  
5222246612  
2017220000  
NUMBER2: 0  
0  
0  
0  
0  
2024642606  
0000100000  
CDBX: N00P  
1SHIP: N00P  
ARX01F  
ARMD XACC1  
ARMD YACC1  
MDAR SCL  
ARMD SSCL  
MDAR RLTI  
JPLS \*\*2  
JUMP \*\*2

TRACKING

[ MILES, TIME = .  
[NEW X,Y VALUES

[ SEC

END OF DATA



26.1	ARAR,X RLT1	
26.2	MDAR GONE1	
26.3	JPLS 1SH5	
26.4	MDAR EXP1	
26.5	JPLS 1SH4	
26.6	MDAR HYP41	
26.7	JPLS 1SH6	
26.10	MDAR HYP51	
26.11	JPLS 1SH7	
26.12	SWITCHOFF (SPR1,TURNR1)	
26.13	MDAR YAW1	
26.14	MDAS,N YAW	
26.15	ARMJ YAW1	
26.16	TURNR4: JPSR SNC0S	
26.17	MDAR,L:PTEN: 20	
26.20	15420VH CESN	
26.21	N00P	
26.22	ARAR,H	
26.23	ARMJ XVEL1	
26.24	MDAR PTEN	
26.25	15420VH SINE	
26.26	N00P	
26.27	ARAR,H	
26.30	ARMJ YVEL1	
26.31	JUMP TURNR3	
26.32	TURNR1: MDAR THET2F	
26.33	JPLS .+2	
26.34	JUMP .+4	
26.35	MDAR YTANF	
26.36	JPLS TURNR4-3	
26.37	JUMP TURNR5	
26.40	SWITCH6FF (SPL1,TURNR2)	
26.41	TURNR5: MDAR YAW1	
26.42	MDAS YAW	
26.43	ARMJ YAW1	

[THIS IS THE TURN ROUTINE



27.1	JUMP TURNR4	
27.2	TURNR2: MDAR YAW1	
27.3	JPSR SNC0S	
27.4	TURNR3: SWITCHOFF (SPT1, INEXT) [END 9F TURN	
27.5	INEXT: N00P	
27.6	1SH4: MDAR XP0S1	
27.7	MDAS XVEL1	
27.10	ARMD XP0S1	
27.11	MDAR YP0S1	
27.12	MDAS YVEL1	
27.13	ARMD YP0S1	
27.14	MDAR HYRT1	
27.15	JPLS *+2	
27.16	JUMP *+2	
27.17	ARAR'X HYRT1	
27.20	MDAR EXP1	
27.21	JPLS 1SH3	
27.22	1SH10: N00P	
27.23	1SH40: N00P	
27.24	1SH42: N00P	
27.25	1SH41: N00P	
27.26	1SH11: N00P	
27.27	1SH2: N00P	
27.30	SWITCHOFF (SPF1, 1SH20)	
27.31	1SH2P: MDAR TPCT1	
27.32	MDAR TPCT1	
27.33	JPLS *+2	
27.34	JUMP 1SH20	
27.35	MDAR RLT1	
27.36	JPLS 1SH20	
27.37	MDAR FLG0NE	
27.40	ARMD IBLK+10 [FER MANJAL FIRE, TORP2F	
27.41	MDAR RLT	
27.42	ARMD'N RLT1	
27.43	JUMP 1SH99	



30.1	1SH20: N00P
30.2	SHIP1XPTR: 0
30.3	SHIP1PTR: 0
30.4	SHIP1ROT: N00P
30.5	1SH20A: N00P
30.6	1SH21: N00P
30.7	1SH5: N00P
30.10	1SH6: N00P
30.11	1SH7: N00P
30.12	1SH30: N00P
30.13	1SH14: N00P
30.14	1SH3: N00P
30.15	JUMP 1SH99
30.16	SSCL2: 1000
30.17	YAWC2: 100
30.20	1SH99:
30.21	2SHIP: N00P
30.22	ARX0'F
30.23	ARMD XACC2
30.24	ARMD YACC2
30.25	MDAR RL12
30.26	JPLS *+2
30.27	JUMP *+2
30.30	ARAR'X RL12
30.31	MDAR GNE2
30.32	JPLS 2SH5
30.33	MDAR EXP2
30.34	JPLS 2SH4
30.35	MDAR HYP2
30.36	JPLS 2SH6
30.37	MDAR HYP2
30.40	JPLS 2SH7
30.41	SWITCHOFF (SPR2,*+4)
30.42	MDAR YAW2
30.43	MDAS'N YAWC2
30.44	ARMD YAW2





SWITCHOFF (SPL2,.,+4)  
 MDAR YAW2  
 MDAS YAWC2  
 ARMD YAW2  
 MDAR YAW2  
 JPSR SNCBS  
 MDAR'X TVC2  
 JPLS 2SH4  
 MDAR TVC  
 ARMD'N TVC2  
 2SH4: MDAR'H XVEL2  
 MDAE XACC2  
 ARMD'H XVEL2  
 MDAR'H YVEL2  
 MDAE YACC2  
 ARMD'H YVEL2  
 MDAR XPSS2  
 MDAS XVEL2  
 ARMD XPSS2  
 MDAR YPSS2  
 MDAS YVEL2  
 ARMD YPSS2  
 MDAR HYRT2  
 JPLS .+2  
 JUMP .+2  
 ARAR'X HYRT2  
 MDAR EXP2  
 JPLS 2SH3  
 JUMP 2SH99  
 2SH10: N00P  
 2SH40: N00P  
 2SH42: N00P  
 2SH41: N00P  
 2SH11: N00P  
 2SH2: N00P

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2SH2P: N00P  
2SH20: N00P  
SHIP2XPTR: 0  
SHIP2PTR: 0  
SHIP2R0T: N00P  
2SH20A: N00P  
2SH21: N00P  
2SH5: N00P  
2SH6: N00P  
2SH7: N00P  
2SH30: N00P  
2SH14: N00P  
2SH3: N00P  
2SH99: 0  
C0LL: N00P  
CSW: SWITCH0FF (SPH2,N0C0L)  
JUMP N0C0L  
CXP0S: 0  
CYP0S: 0  
CTHETA: JUMP .  
ARX0'F  
ARM0 XTANF  
ARM0 YTANF  
ARM0 TANF  
MDAR'H XPO'S1  
17440VH 3  
ARM0'H CXP0S  
MDAR'H XPO'S2  
17440VH 3  
MDAE'N'H CXP0S  
ARM0'H'N  
MDAR'H YPO'S1  
17440VH 3

CXP0S

PROGRAM CALCULATE THETA

IZERO OUT FLAG



33.1	ARM'D'H CYP@S	
33.2	MDAR'H YP@S2	
33.3	1740VH 3	
33.4	MDAE'H'H CYP@S	
33.5	ARM'D'H'N CYP@S	
33.6	MDAR'H'N CXP@S	
33.7	JPAN *+3	[TEST FOR X NEG
33.10	MDAR FL@NE	
33.11	ARM'D XTANF	
33.12	MDAR'H'N CYP@S	[TEST FOR Y NEG
33.13	JPAN *+3	
33.14	MDAR FL@NE	
33.15	ARM'D YTANF	
33.16	MDAR'H CYP@S	
33.17	ANX@ MZR@	[MAKE P@S
33.20	ARM'D'H CYP@S	
33.21	MDAR'H CXP@S	
33.22	ANX@ MZR@	
33.23	ARM'D'H CXP@S	
33.24		
33.25	MDAR'H CYP@S	
33.26	MDAE'H'N CXP@S	
33.27	JPAN *+8.	
33.30	MDAR FL@NE	
33.31	ARM'D TANF	
33.32	MDAR'H CXP@S	
33.33	16420VH CYP@S	[X2-X1/Y2-Y1
33.34	N@PP	
33.35	ARM'D TANT	
33.36	JUMP *+6	
33.37	MDAR'H CYP@S	
33.40	16420VH CXP@S	[Y2-Y1/X2-X1
33.41	N@PP	
33.42	ARM'D'L	
33.43	TANT: 0	



34.1	ARAR'H'N'F		
34.2	J'PAN *+2		
34.3	ARMD'H TANT		
34.4			
34.5	TNT1: MDAR THETAT		
34.6	JPSR SNGGS		
34.7	MDAR COSN		
34.8	JPLS *+2		
34.9	JUMP TANEND		
34.10	MDAR SINE		
34.11	JPLS *+2		
34.12	JUMP TANEND		
34.13	MDAR'H SINE		
34.14	16+20/H COSN		
34.15	N99P		
34.16	MDAS'N TANT		
34.17	MDAS'N'L		
34.18	DELTA'N: 200		
34.19	JPLS *+2		
34.20	JUMP TANEND		
34.21	ARAR'H'F		
34.22	J'PAN *+5		
34.23	MDAR THETAT		
34.24	MDAS'N DELTH		
34.25	ARMD THETAT		
34.26	JUMP TNT1		
34.27	ARAR'H'F		
34.28	MDAS DELTAN		
34.29	MDAS DELTAN		
34.30	ARAR'H'N'F		
34.31	J'PAN *+5		
34.32	MDAR THETAT		
34.33	MDAS DELTH		
34.34	ARMD THETAT		
34.35	JUMP TNT1		
34.36	ARAR'H'N'F		
34.37	J'PAN *+5		
34.38	MDAR THETAT		
34.39	MDAS DELTH		
34.40	ARMD THETAT		
34.41	JUMP TNT1		
34.42			
34.43			

	[TEST F9R DIV ERRORS
	[TEST F9R COSN = 0
	[TEST F9R SIN = 0
	[SINE/COSN
	[TEST F9R DELTAN = 0
	[THETA T9 LARGE
	[THETA T9 SMALL
	[SINE THETA IN SINE





35.1	TANEND: MDAR SINE	
35.2	JPLS .+4	CTEST FOR SIN = 0
35.3	ARX9'F	
35.4	ARMD THETAT	
35.5	JUMP .+5	
35.6	MDAR COSN	
35.7	JPLS .+3	
35.10	MDAR THZMAX	
35.11	ARMD THETAT	
35.12		
35.13	MDAR THETAT	
35.14	ARMD THETA	
35.15		
35.16	MDAR TANF	CTEST FOR TAN GT 45
35.17	JPLS .+2	
35.20	JUMP .+4	
35.21	MDAR HFS	CT20000
35.22	MDAS'N THETA	
35.23	ARMD THETA	
35.24	MDAR XTANF	CTEST FOR NEG X
35.25	JPLS .+2	
35.26	JUMP .+4	
35.27	MDAR FS	
35.30	MDAS'N THETA	
35.31	ARMD THETA	
35.32	MDAR YTANF	
35.33	JPLS .+2	
35.34	JUMP .+3	CTEST FOR Y NEG
35.35	MDAR'H'N THETA	
35.36	ARMD'H THETA	
35.37		
35.40	MDAR THETA	
35.41	JPSR SNCOS	
35.42	MDAR COSN	
35.43		
35.44	JPLS .+2	



36.1	JUMP *+5	[FOUND A GOOD THETA
36.2	MDAR'H SINE	
36.3	JPAN *+10	
36.4	ARAR'H'F	
36.5	JPLS *+4	
36.6	MDAR CXP8S	
36.7	MDAS CYP8S	
36.10	JUMP *+6	
36.11	MDAR'H CYP8S	
36.12	JUMP *+2	
36.13	MDAR'H'N CYP8S	
36.14	16420VH SINE	[Y/SIN THETA = Z
36.15	N89P	[SHIFT 1 RIGHT
36.16	17440VH 1	
36.17	MDAR'IA THZMAX	
36.20	ARMD MILES	
36.21		
36.22	MDAR MILES	
36.23	ARBR'F	
36.24	MDIR CTHETA	
36.25	THZMAX: 37777	DISPLAY MILES
36.26	DELTH: 20	
36.27	CTH'F: 0	
36.30	THETA: 0	
36.31	THETAT: 0	
36.32	XTANF: 0	
36.33	YTANF: 0	
36.34	TANF: 0	
36.35	N8CSL: N89P	
36.36	TRYSTARS3: N89P	
36.37	N8STARS: N89P	
36.40	TRP1: N89P	
36.41	JUMP TRP91	
36.42	TRP30: N89P	
36.43	TRP31: N89P	



37.1	TRP5: N00P
37.2	TRP51: N00P
37.3	TRP52: N00P
37.4	TRP41: N00P
37.5	TRP40: N00P
37.6	TRP42: N00P
37.7	TRP90: N00P
37.10	TRP91: N00P
37.11	MSTAR: N00P
37.12	STRMVPTR: 0
37.13	STARMOVE: N00P
37.14	DNE: MDAR'N STRTF
37.15	JPAN DNE1
37.16	MDBR'N ZER0
37.17	JUMP OUTPUT
37.20	TCOUNT: 0
37.21	DNE1: MDAR STRTF
37.22	JPAN OUTPUT
37.23	JUMP DNE1
37.24	OUTPUT: ARAR'X TCOUNT
37.25	MD07'L: 0
37.26	JPSR ST0VER
37.27	OUT1: N00P
37.30	JPSR C0FF
37.31	MD10'L'0
37.32	CLKEN
37.33	JUMP L00P
37.34	OUT2: OUT1
37.35	OUT3: N00P
37.36	JUMP'1 OUT2
37.37	SHIP1:
37.40	SHIP2:
37.41	SHIPX:
37.42	SHIP2X:
37.43	STARS: -2



```

40.1 STARS1: 0
40.2 STARS2: 0
40.3 STARS3: 0
40.4 STARSND: 0
40.5
40.6 S0=77777;S77777=77777
40.7 S1=1444;S2=3106;S3=4544;S4=6174
40.10 S5=7615;S6=11224;S7=12620;S10=14176
40.11 S11=15535;S12=17053;S13=20347;S14=21616
40.12 S15=23040;S16=24232;S17=25373;S20=26501
40.13 S21=27554;S22=30571;S23=31550;S24=32467
40.14 S25=33345;S26=34161;S27=34733;S30=35441
40.15 S31=36102;S32=36477;S33=37025;S34=37305
40.16 S35=37517;S36=37661;S37=37754;S40=37777
40.17 AM=77777
40.20 MACR01 QUADRANT(VAL1,VAL2)
40.21 LOC(.,+40);ENDM
40.22 MACR02 QUADRANT(VAL1,VAL2)
40.23 CC=0
40.24 REPEAT 40
40.25 DD=40-CC
40.26 VAL1 AM/JH (VAL2 AM)
40.27 CC=CC+1
40.30 ENDR
40.31 ENDM [REM
40.32
40.33 SNC0S:JUMP .
40.34 MDAR'IA AMSK
40.35 ARMD'IH DEL
40.36 17440/JH 8.
40.37 MDAS'IF'N 77
40.40 JPAN .+3
40.41 ARAR'IN'F
40.42 MDAS'IF 1
40.43 MDAE'IL; MDAR TBL+77
40.44 ARIR'F

```





41.1	ARM D PT
41.2	MDAR DEL
41.3	JPAR SN1
41.4	MDAR'A'F'H 377
41.5	ARM D FLAG
41.6	SNO:
41.7	16440VH 12137
41.10	N88P
41.11	ARM D DEL
41.12	15420VH DEL
41.13	N88P
41.14	ARAR'N'H'F
41.15	MDAS'F 37777
41.16	ARM D F1
41.17	15420VH PT
41.20	AMSK: N88P 77777
41.21	ARM D C8S
41.22	MDAR'N DEL
41.23	15400VH PT
41.24	N88P
41.25	MDAF C8S
41.26	MDAR'A'H AMSK
41.27	ARM D'H C8S
41.30	MDAR F1
41.31	15400VH PT
41.32	N88P
41.33	ARM D SIN
41.34	MDAR DEL
41.35	15420VH PT
41.36	N88P
41.37	MDAE SIN
41.40	MDXG'H FLAG
41.41	MDAR'A'H AMSK
41.42	ARM D'H SIN
41.43	MDIR SNC8S



SN1: MDX9'F'H 377  
 MDAR'A'F'H 377  
 ARMD'E FLAG  
 JUMP SNO  
 TBL:  
 QUADRANT(SN CC,S\ DD)  
 QUADRANT(SN DD,-S\ CC)  
 SINE:  
 SIN: 0  
 C9SN:  
 C9S: 0  
 F1: 0  
 PT: 0  
 DEL: 0  
 FLAG: 0  
 EXP9F: N99P  
 EXP9F1: N99P  
 RCOUNT1: 0  
 RCOUNT2: 0  
 RIEST: 0  
 RSUM: 0  
 RAYSK: 37777

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```
[YP9S1
[THETA1
[XP9S2
[YP9S2
[THETA2
[MILES
[THETA
[TP9P2F

[INIT
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PHASE 0: ACCELERATE MOUNT, START SPEEDUP
PHASE 1: DEACCELERATE MOUNT, CONTINUE SPEEDUP
PHASE 2: ZERO ACCELERATION, MAXIMUM SPEED REACHED
PHASE 3: DEACCELERATE MOUNT, START SLOW DOWN
PHASE 4: ACCELERATE MOUNT, CONTINUE SLOW DOWN
PHASE 5: SWITCH INTO LINEAR MODE

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300

THE FOLLOWING PROGRAM SEGMENT IS USED TO SETUP THE INTERFACE BETWEEN THE ADAGE SYSTEM AND THE XDS - 9300 COMPUTER





```

C   LET IBLK BE DATA FOR AGT1
C   LET JBLK BE DATA FOR AGT2
  DIMENSION IBLK( 64),JBLK( 64)
  IER = 0
  IC9M = 15
  DO 1000 I = 1, 64
    JBLK(I) = 5
    1000 IBLK(I) = 0
    PRINT 1001
    1001 FORMAT (1H1)
    1010 PRINT 1101, (IBLK(I),I=1, 10),IDEV
    PRINT 1101, (JBLK(I),I=1, 10),IDEV
    1101 FORMAT(10(1X,08),110)
    1102 FORMAT(10(1X,08),110/)
    PAUSE
    IDEV = 1
    1003 CALL AGTC9M(IDEV,IC9M,IER,IBLK,10 ,JBLK,10 )
    PRINT 1200,IER
    1200 FORMAT (// , THE ERROR IS SF TYPE',I2,/)
    PRINT 1101, (JBLK(I),I=1, 10),IDEV
    PRINT 1101, (IBLK(I),I=1, 10)
    IF (IER.NE. 0) GO TO 1099
    1002 IDEV = 2
    CALL AGTC9M(IDEV,IC9M,IER,IBLK,10 ,JBLK,10 )
    PRINT 1200,IER
    PRINT 1101, (IBLK(I),I=1, 10)
    PRINT 1101, (JBLK(I),I=1, 10),IDEV
    IF (IER.NE. 0) GO TO 1099
    1004 CONTINUE
  S   LDA =100000
    CALL DELAY
    PRINT 1101, (IBLK(I),I=1, 10)
    PRINT 1102, (JBLK(I),I=1, 10)
    1099 PRINT 1199
    1199 FORMAT(//,3X, ' AN ERROR HAS BEEN MADE' //)

```



C C

```

DIMENSION A(25)
REAL K1,K2
LOGICAL IFLAG
NAMELIST IREP,THR,K1,K2,SW1,SW2,SW3,SW4,SW5
C READ IN DATA CARDS FOR PBTSET
READ 100,(A(I),I=1,20)
100 FORMAT(10F8.4)
C SET APPROPRIATE PBTs
CALL SETPBT (4HP001,A(1),4HP002,A(2), 4HP003,A(3), 4HP004,A(4))
CALL SETPBT (4HP005,A(5),4HP006,A(6), 4HP007,A(7), 4HP010,A(8))
CALL SETPBT (4HP011,A(9),4HP012,A(10),4HP013,A(11),4HP014,A(12))
CALL SETPBT (4HP015,A(13),4HP016,A(14),4HP017,A(15),4HP024,A(20))
CALL SETPBT (4HP020,A(16),4HP021,A(17),4HP022,A(18),4HP023,A(19))
E = .001
C K1 IS THE VALUE FOR K
K1 = 2.5
AA= K1/25.
C SET VARIABLE PBT, DEPENDS UPON K
CALL SETPBT(4HP047,AA)
K2 = .9999
SW1 = 0.
SW2 = .9
SW5 = -.0
IREP =2000
THR = .5
1 IF (SENSE SWITCH 1) 10,2
2 IF (SENSE SWITCH 2) 30,3
3 IF (SENSE SWITCH 3) 30,4
4 IF (SENSE SWITCH 4) 40,5
5 CALL HOLD
S2 = SW2
SW3 = THR - .4*THR
SW4 = THR - .2*THR

```



```

THP = -K2
IFLP = 1
IPHASE = 0
CHG = THR
ICOUNT=0
IF (SENSE SWITCH 5) 82,83
82 PRINT 200
200 F9MAT (1H1)
83 IF (SENSE SWITCH 6) 84,85
84 PRINT 201
201 F9MAT (///)
85 CALL RESET(1000)
   SETS UP INTERRUPT SYSTEM
CONNECT (052,ENDSTP(IFLAG))
IFLAG = .TRUE.
CALL COMPUTE
C   TEST(13) IS AN ANALOG INTERRUPT
121 IF (TEST(13) .GT. 0) G9 T9 121
CALL ENABLE
CALL DISABLE
READ THTA,THAD,THADD
   T500 T501 T502
   A-17 A-15 A-13
7 CALL ADL(0,THTA,THAD,THADD)
IF (SENSE SWITCH 5) 77,78
77 CALL HOLD
PRINT 110,THTA,THAD,THADD,TH,THP,THF,IPHASE,ICOUNT
110 F9MAT( , THTA ='F 7.4,' THAD ='F 7.4,' THADD ='F 7.4,
1 , ERROR ='F7.4,' THP='F7.4,' THF='F7.4,' IPHASE ='I2,
2 , TIME ='I4)
CALL COMPUTE
178 IF (TEST(13) .GT. 0) G9 T9 178
78 ICOUNT=ICOUNT+1
THF = -THTA

```



```

IP=IPHASE+1
G9 T9 (18,8,9,24,23,25),IP
      PHASE ZERO
18 IF (THTA.GE.CHG) G9 T9 32
   IF (THTADD.LT.S2 ) G9 T9 80
      THP = .0
      IPHASE = 1
   IF (SENSE SWITCH 6) 160,80
160 CALL HOLD
      PRINT 110,THTA,THTAD,THTADD,TH,THP,THF,IPHASE,IC9UNT
      CALL COMPUTE
170 IF (TEST(13).GT. 0) G9 T9 170
   G9 T9 80
      PHASE ONE
8 IF (THTA.GE.CHG) G9 T9 32
   IF (THTADD.GT. .30) G9 T9 80
   IF (-THTAD.GE. .6 ) G9 T9 31
   IF (THTA.GT. SW3) G9 T9 31
      IPHASE = 0
      THP = -K2
   IF (SENSE SWITCH 6) 161,80
161 CALL HOLD
      PRINT 110,THTA,THTAD,THTADD,TH,THP,THF,IPHASE,IC9UNT
      CALL COMPUTE
171 IF (TEST(13).GT. 0) G9 T9 171
   G9 T9 80
31 THP = 0
      IPHASE = 2
   IF (SENSE SWITCH 6) 162,88
162 CALL HOLD
      PRINT 110,THTA,THTAD,THTADD,TH,THP,THF,IPHASE,IC9UNT
      CALL COMPUTE
172 IF (TEST(13).GT. 0) G9 T9 172
88 S2 = SW2 / 2.
   IF (IFLP.EQ. 0) G9 T9 80

```





```

      CHG = THR -      THTA
      IFLP = 0
      GO TO 80
C
      PHASE TWO
      9 IF (-THAD,GE,.5 ) GO TO 32
      IF (THTA,GT,SW4) GO TO 32
      IPHASE = 0
      THP = -K2
      IF (SENSE SWITCH 6) 163,80
      163 CALL HOLD
      PRINT 110,THTA,THAD,THADDO,TH,THP,THF,IPHASE,ICOUNT
      CALL COMPUTE
      173 IF (TEST(13) .GT. 0) GO TO 173
      GO TO 80
      32 THP = 0
      IF (THTA,LT. CHG) GO TO 80
      THP = K2
      IPHASE = 3
      S2 = THAD
      IF (SENSE SWITCH 6) 164,80
      164 CALL HOLD
      PRINT 110,THTA,THAD,THADDO,TH,THP,THF,IPHASE,ICOUNT
      CALL COMPUTE
      174 IF (TEST(13) .GT. 0) GO TO 174
      GO TO 80
C
      PHASE THREE
      24 IF ( THADDO,GT. S2 ) GO TO 80
      THP = -K2
      IPHASE = 4
      IF (SENSE SWITCH 6) 165,80
      165 CALL HOLD
      PRINT 110,THTA,THAD,THADDO,TH,THP,THF,IPHASE,ICOUNT
      CALL COMPUTE
      175 IF (TEST(13) .GT. 0) GO TO 175
      GO TO 80

```



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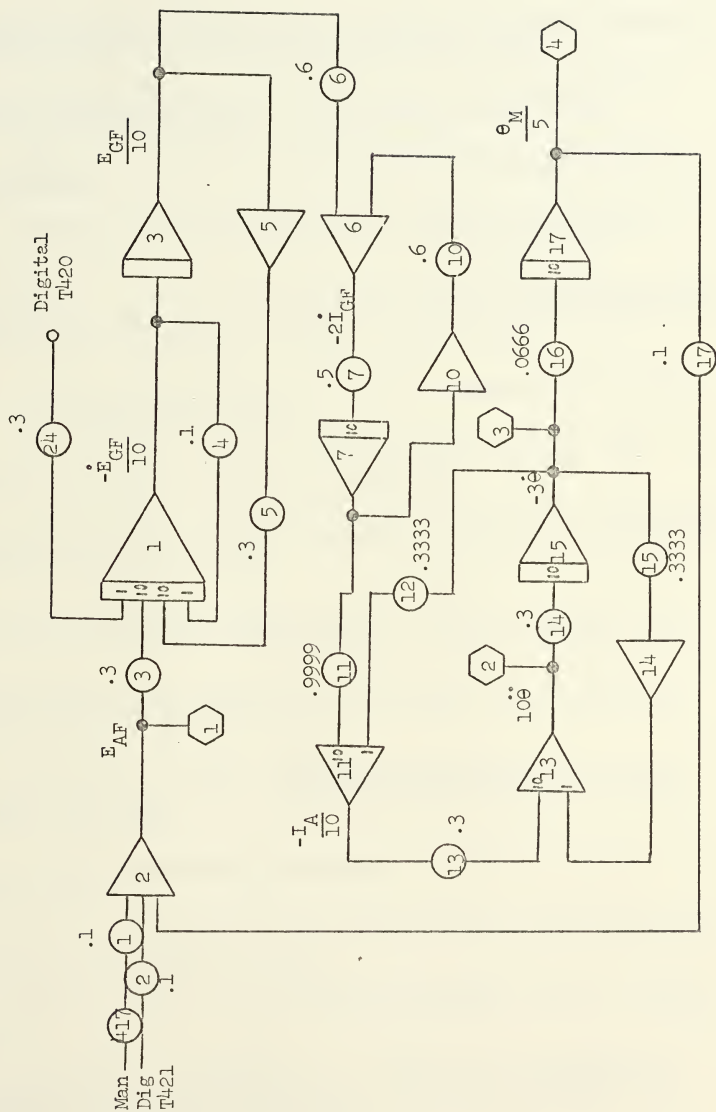
C      PHASE FOUR
23 IF ( THTADD .LT.--.40)G9 T9 80
    THP = -K2/2.
    IF ( THTADD .LT.--.1) G8 T9 80
C      PHASE FIVE
25 THP = 0
    THF = -THR
    IPHASE = 5
    IF (SENSE SWITCH 6) 166,80
166 CALL HBLD
    PRINT 110,THTA,THTAD,THTADD,TH,THP,THF,IPHASE,IC9UNT
    CALL COMPUTE
176 IF (TEST(13) .GT. 0) G8 T9 176
80 TH = ABS(THR-THTA)
    IF (TH .LE. E) G8 T9 90
    IF (THTA .LT. SW3) G8 T9 81
    IF (IPHASE .NE. 2) G9 T9 81
    IF (THTA .GT. CHG) G8 T9 32
81 IF (SENSE SWITCH 1) 10,11
11 IF (SENSE SWITCH 2) 20,12
12 IF (SENSE SWITCH 3) 30,13
13 IF (SENSE SWITCH 4) 40,14
14 IF (IREP.LE.IC9UNT) G9 T9 90
    CALL DAC(1,-THP,2,THF)
    G9 T9 7
10 OUTPUT(101)'IREP'
    INPUT(101)
    G9 T9 5
20 OUTPUT(101)'THR'
    INPUT(101)
    G9 T9 5
30 OUTPUT(101)'K1'
    INPUT(101)
    G9 T9 5
40 OUTPUT(101)'K2'

```













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| <p>Techniques are developed for the design of a monitor of a real-time multi-computer system that is under heavy loading. The first portion relates to the requirements of partitioning to aid in fault recognition and diagnostic routines. The dynamic allocation of system time to the system tasks and fault monitoring is developed secondly. System reconfiguration of the partitioned subsystems restores the system to operation at a degraded level until faults are corrected. The paper discusses a Ship Combat Weapon System as an example of a large scale multi-computer system monitor.</p> |  |                                                                             |                 |



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|    | Diagnostic routines              |        |    |        |    |        |    |
|    | Real-Time computer               |        |    |        |    |        |    |
|    | Dynamic time allocation          |        |    |        |    |        |    |
|    | Ship combat weapon system        |        |    |        |    |        |    |
|    | Multi-Computer system monitor    |        |    |        |    |        |    |
|    | Partitioning and reconfiguration |        |    |        |    |        |    |

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